

X570 AORUS PRO WIFI

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2015.12.30~B.L

GIGABYTE™

Title

Size

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COVER SHEET

Model Name : X570 AORUS PRO WIFI


Component value change history Version: 1.0 P-Code: U98126-0

Date	Change Item	Reason
2017.12.19	1.0A E-BOM Release. PCB: 11	1. AUDIO JACK net name change to LINE2_JD 2. DEL 110A , 80B SMD 螺柱(BOM) 3. M2B_SOCKET CLK change to GPP_CLK4 4. RC MATCH: DA_DAR7 = DB_DAR7 = DC_DAR7 = DD_DAR7 = DE_DAR7 = DF_DAR7 = DG_DAR7 = DH_DAR7 = DI_DAR7 = DJ_DAR7 = 13.7K DAC16=18pF,DAR39=DAR35=23.7K,DAR40=30K 5. Audio 裝甲led改為側發光 6. REAR_BUTTON SCH 修改(ERP ISSUE自動POWER ON) 7. UPDATE M.2 NS P/N 8. M2B_SOCKET CLK change to GPP_CLK4P/N 9. remove RST_SW 10. VU1 OV change to PM_1V05 & PM_2V5 11. LED_CPU footprint change to PH1X4-FAN-AMD
2018.01.23	1.0B P-BOM Release. PCB: 101	1. ARN5->150ohm , ARN4/ARN6->82ohm 2. PSWQ1 NA , PSWR3 上件 3. PCB_BAR用料修改 11WC1-020146-13R" 4. WIFI change to intel 9260
2018.01.30	1.0C P-BOM Release. PCB: 101	1. 修改F_USB31C用料10NH5-040210-91R/92R 2. change alo shield 料號
2018.02.12	1.0D F-BOM Release. PCB: 101	1. 下版BOM需修改AUDIO TAP 用料(100u/TAP/6.3V/65)(CEC8,CEC12,CEC7,CEC11,CEC1,) 2. PR10 NA , PR12 上件 (for extrnal clk)
2018.02.22	1.0E F-BOM Release. PCB: 101	1. PR10 上件 , PR12 NA (for LAN issue)
2018.03.20	1.0F F-BOM Release. PCB: 101	1.IR FW update (10TA1-635201-15R)
2018.03.27	1.0G P-BOM Release. PCB: 101	1.修改B_BOARD用料11KRC-020001-42R
2019.02.22	0.1	1. EVT release
2019.03.22	0.2	1. EVT release
2019.03.22	0.3	1. M_BIOS SMD改成DIP階
2019.03.22	1.0	1. Remove M_BIOS socket
		2. DAJP不上件
		3. PCH_HS & M2 HS要改成PRO專用
		4. USB3.1 ESD IC
2019.05.16	1.0B	1. CHANGE USB ESD IC
2019.05.21	1.0C	1. Remove LED_COVER,LEDC6 2. Q68 2N7002 -> EMF30N02J/SOT23/627pF/30m (Low Vgs)
2019.06.14	1.0D	1. 修改 DAU1 料號為 : 10TA1-635201-27R

Circuit or PCB layout change for next version

Date	Change Item	Reason
2018.01.22	PCB: 101	1. Add MB_ID3 (SIO GP10 , default "L") 2. ATX & 2'4 & 2'2 改實心 (修改 footprint) 3. EC1 VIN5 VPP_MEM change to PM_2V5 4. R_USB30 100U SMD * 4 USB_LAN 560u*1 F_USB30_1&F_USB30_2 560u*2 F_USB20_1 & F_USB20_2 100u*2
2019.02.22	0.1	1. EVT release
2019.02.22	0.2	1. PCIE SLOT change to GEN4 slot
		2. Update Heatsink 料號
		3. VIN CAP 10u/8/X5R -> 10u/8/X6S
		4. 0201 6.3V 1U X5R 加替料 : 10CM9-1M1004-5FR
		5. Set single BIOS (OR56 -> OR58 1K/4/1)
		6. Remove EMC9,,10,12,14,20,21
		7. Q-FLASH ADD 1.8V POWER
		8. ERP leackge issue
		9. GL850 pin23 Add HU2AC9,HU2BC9 For EMI
		11. Update LED_C1,LED_C2 footprint
2019.03.22	0.3	1. PCH CPU HUB TX/RX CAP 下 GND , VIA挖空
		2. M2 CAP 下 GND
		3. CAP離SLOT 500mils
		4. PCIEX16 CPU Vcore Int2/Solder side swap
		5. Q-FLASH Add -PWRBT SW control
		6. M2 GPIO PCH -> IO GP22,23,63,64
		7. CP_3LED1,CP_3LED2 位置修改 for X570 AORUS PRO
		8. Debug LED IO_GP63,IO_GP64 -> IO_GP17,IO_GP20
		9. Update LED_CPU footprint
2019.04.02	0.1	1. X570 AORUS PRO 0.1
2019.04.02	0.2	1. 裝甲REAR_HS footprint updaet "X570_AORUS_PRO_IO_COVER"
		2. 增加PCB圖騰
		3. Remove M2C_SOCKET
		4. LED setting
		5. WIFI BOM不上件
		6. PCIEX4 GEN4 -> GEN3 SLOT
		7. 裝甲料號修改AUDIO COVER不上件
		8. AUDIO 金屬蓋在ULTRA移除

- 2019.05.141.0.11. F_USB31C移至PCH_HS外面
- 2019.03.221.02. 文字面PCH_HS加大,REAR_HS文字面移至後窗3. Fix Picasso CPU can't boot issue.
4. ADD PCIE SLOT, M.2 GPIO AUX reset from CPU.5. FAN GPIO change.



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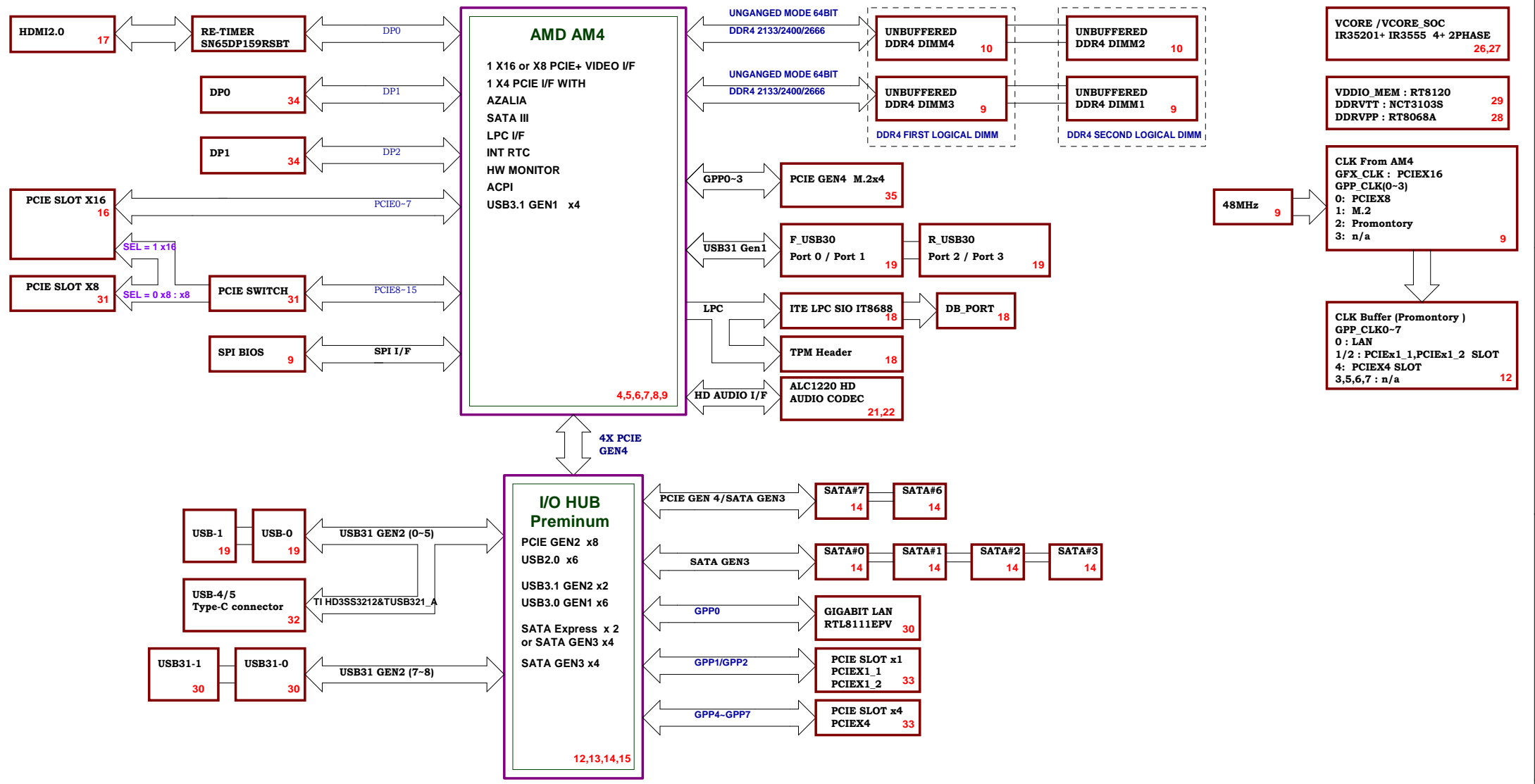
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BOM & PCB HISTORY

X570 AORUS PRO WIFI

Rev

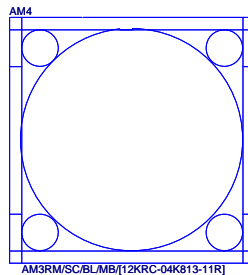
1.0



MEM CHA

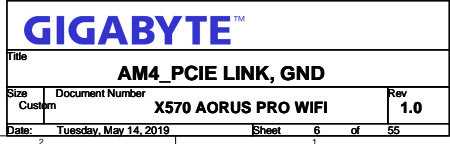


10 MODT_A[0..3] <-- MODT_A[0..3]
10 MDA[0..63] <-- MDA[0..63]
10 MAAA[0..16] <-- MAAA[0..16]
10 DQSA[0..8] <-- DQSA[0..8]
10 -DQSA[0..8] <-- -DQSA[0..8]



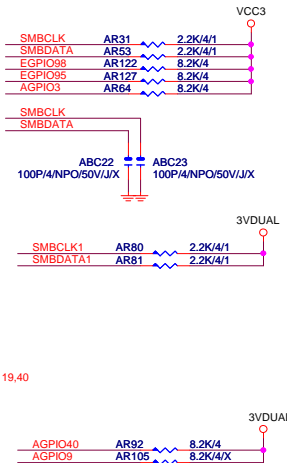
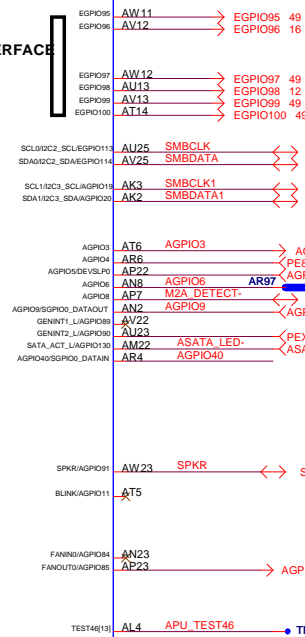
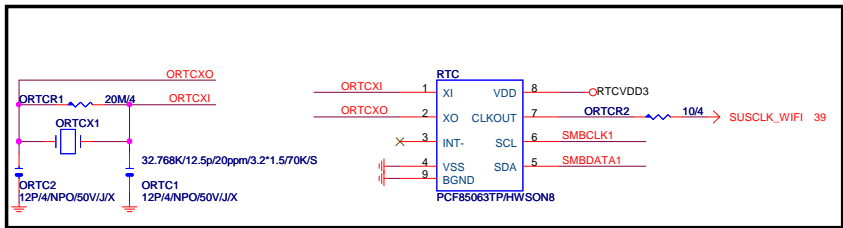
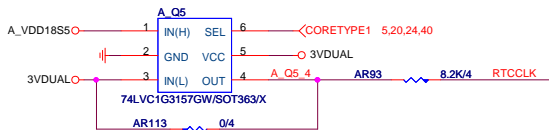
11 MODT_B[0..3] <-- MODT_B[0..3]
11 MDB[0..63] <-- MDB[0..63]
11 MAAB[0..16] <-- MAAB[0..16]
11 DQSB[0..8] <-- DQSB[0..8]
11 -DQSB[0..8] <-- -DQSB[0..8]

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CPU CONTROL			
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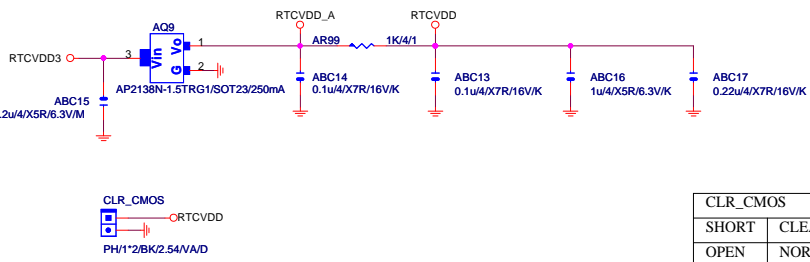
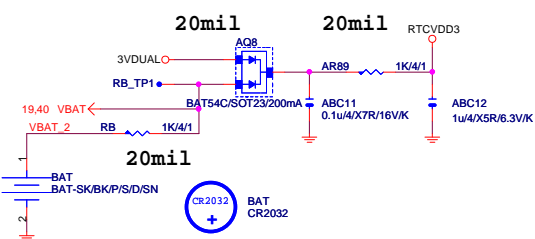
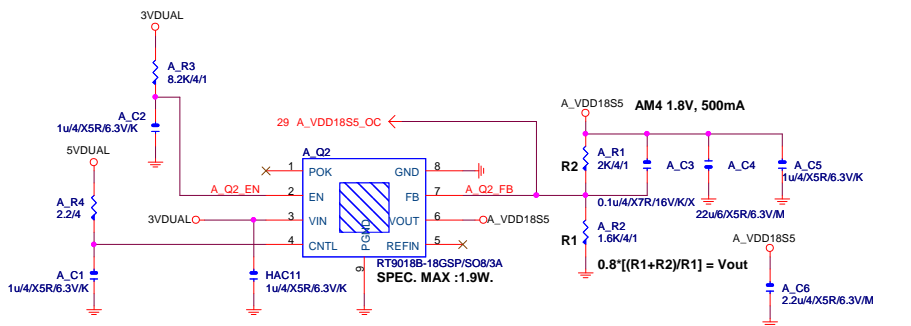
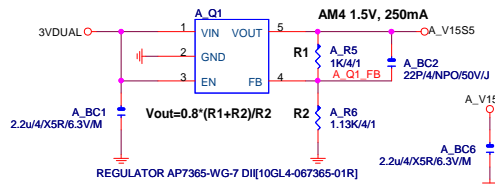
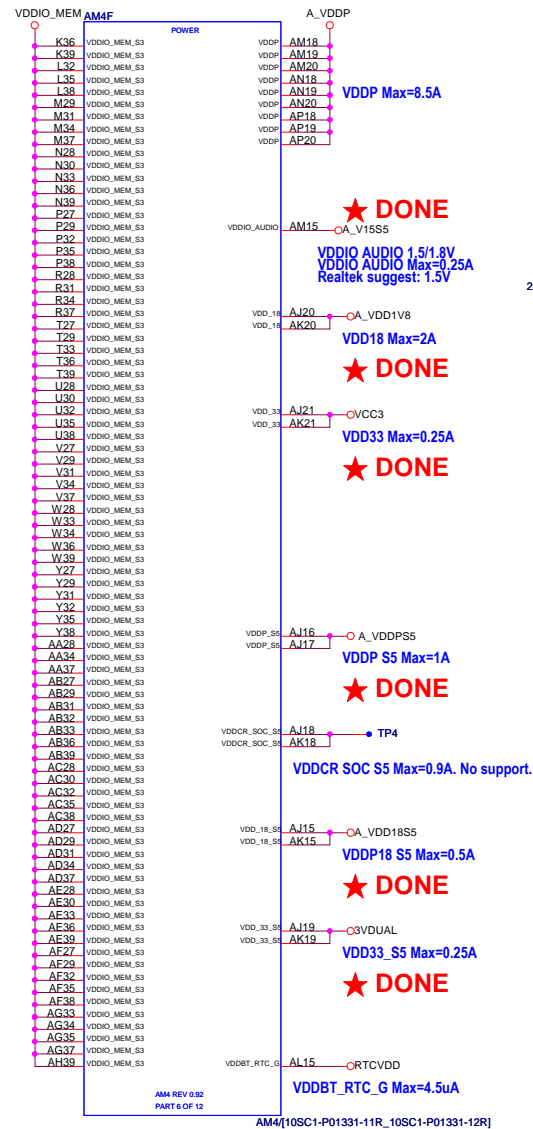


AM4 MISC

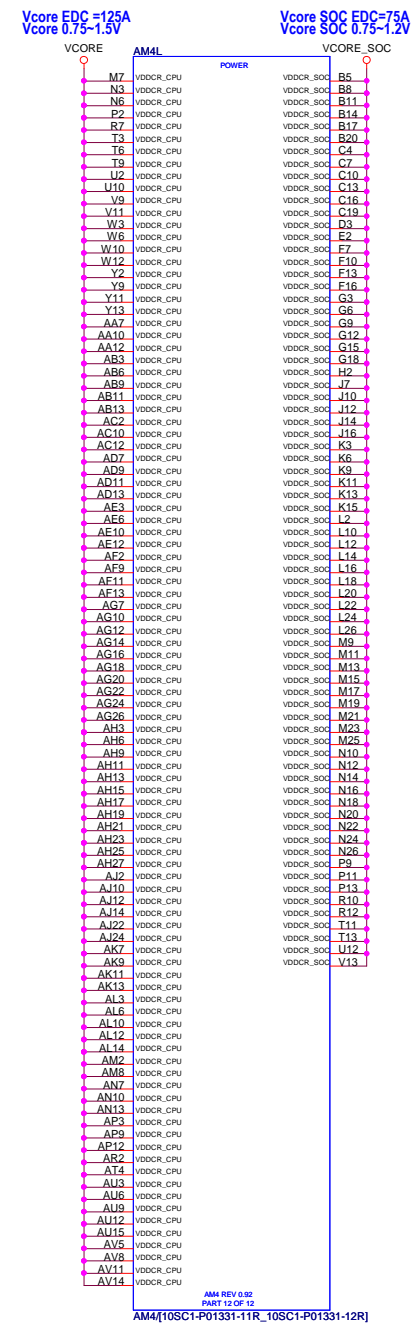
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VDDIO Max=15.5A



CLR_CMOS	
SHORT	CLEAR CMOS
OPEN	NORMAL
NOT ADD ICT FOR RTCVDD PIN	



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CPU POWER & GND

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Date

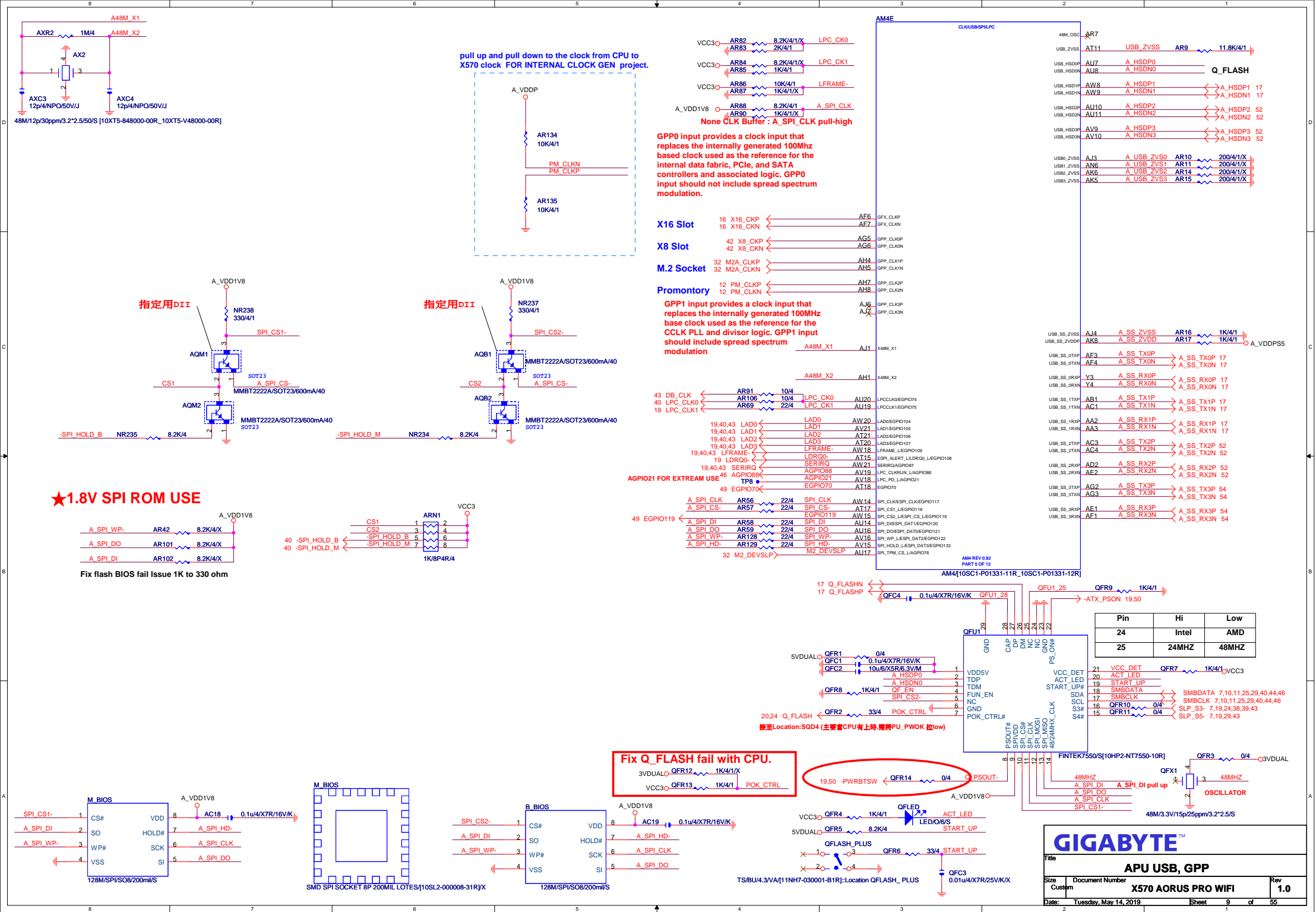
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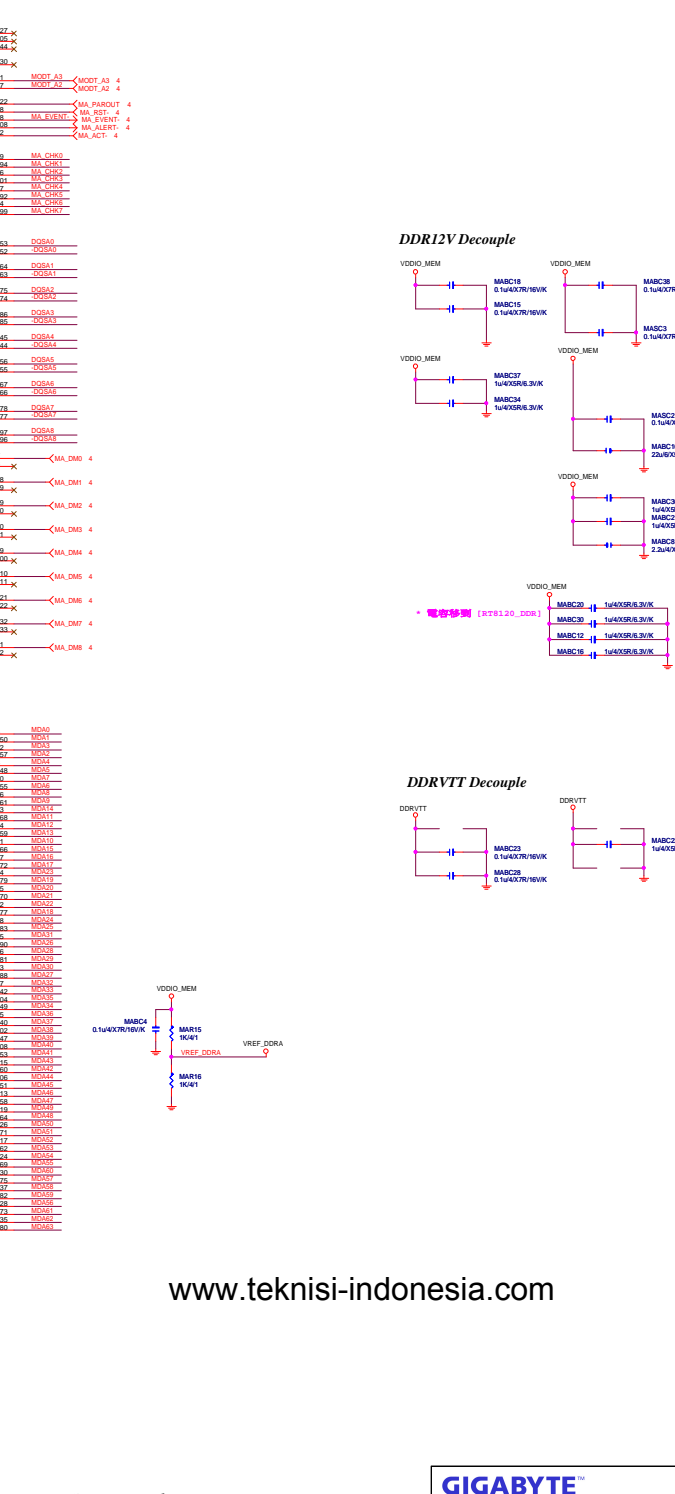
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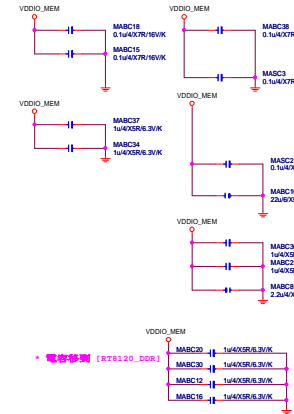
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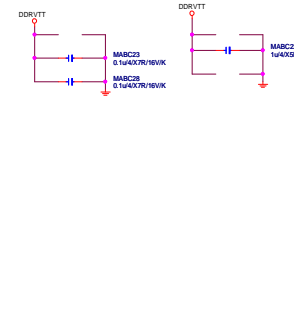




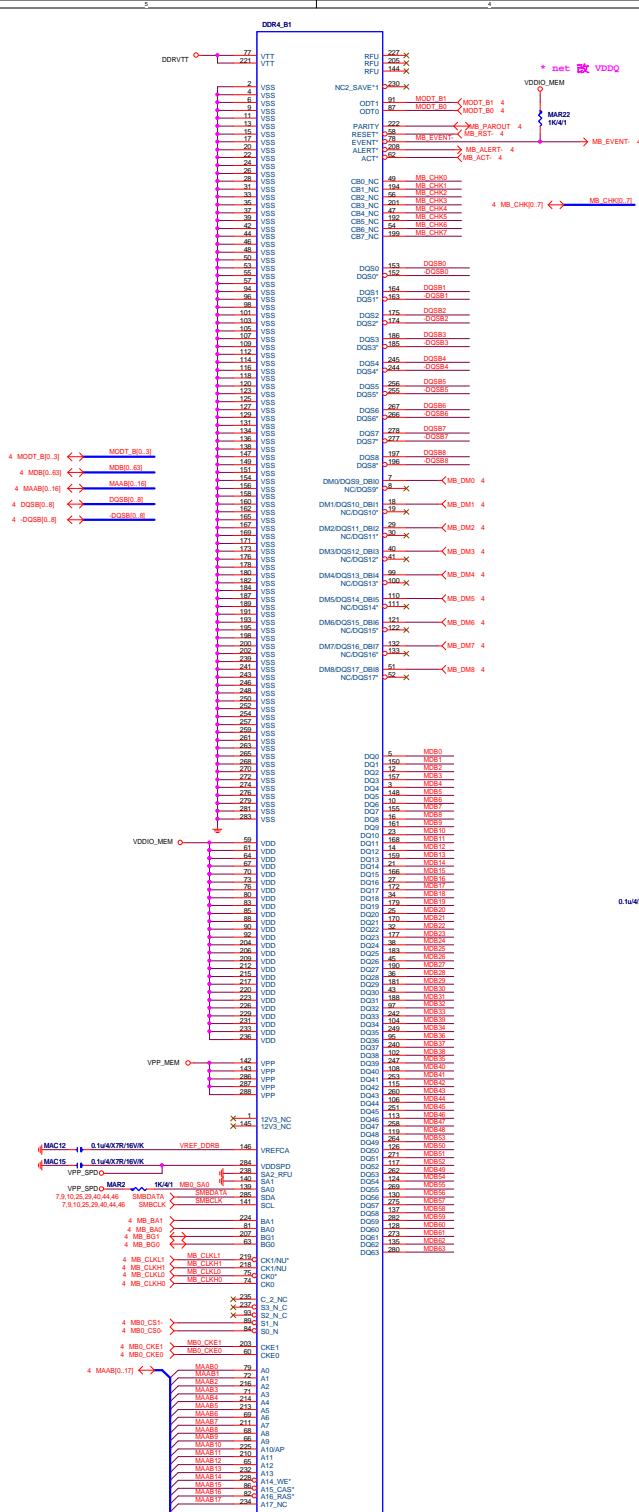
DDR12V Decouple



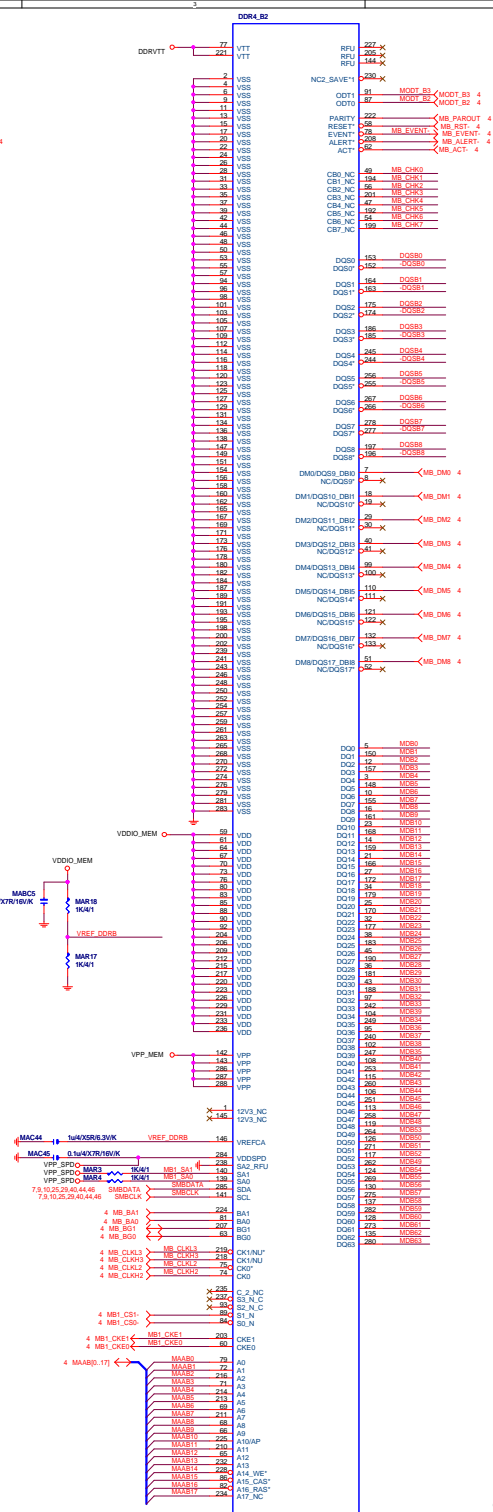
DDRVRTT Decouple



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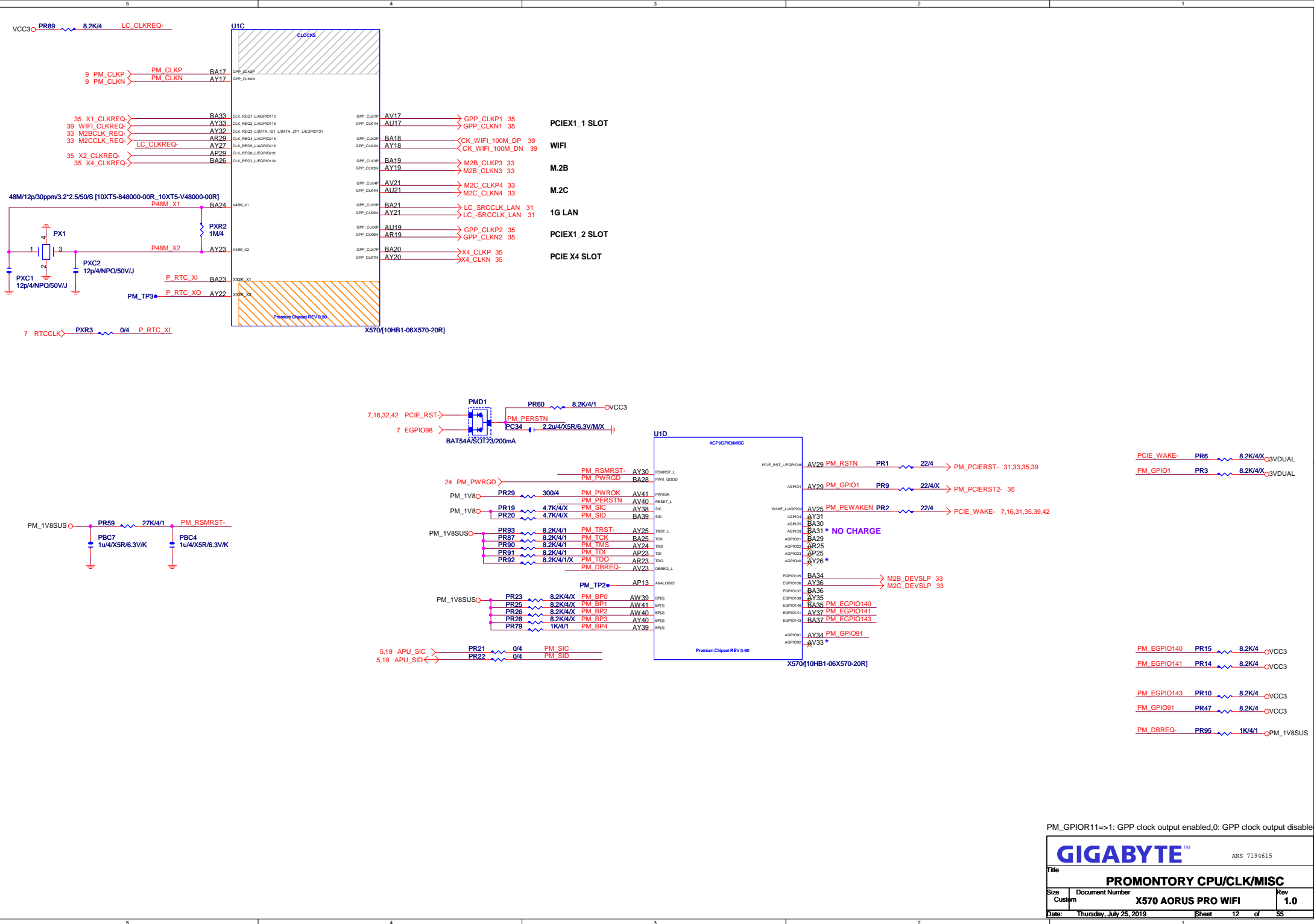


CHANNEL B0
SA2:1=001



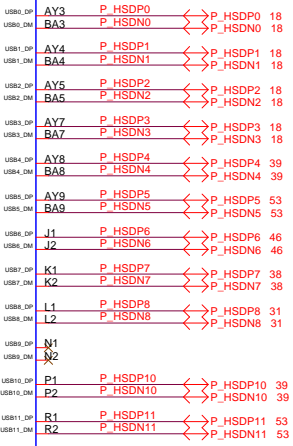
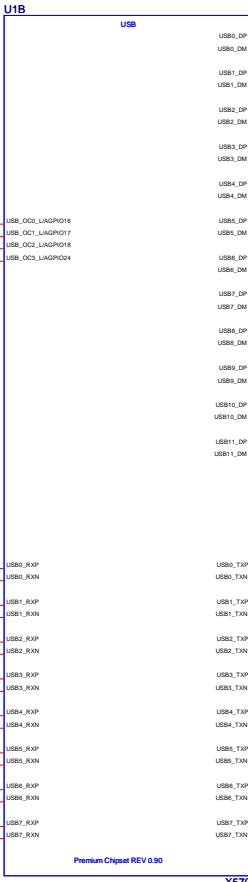
CHANNEL B1
SA2:3=011

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DDR4 CHANNEL B		
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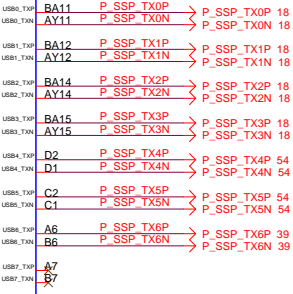
PM_GPIOR11=>1: GPP clock output enabled,0: GPP clock output disabled

18,39 -USBOC_F1>-USBOC_F1 AV27
-USBOC_F1 AU27
-USBOC_R1 AP27
31,38 -USBOC_R1>-USBOC_R1 AY28



F_USB 30_2
F_USB 30_2
F_USB 30_1
F_USB 30_1
M2_WIFI
R_USB2 x4 HUB
MCU
R_TYPE C
1GLAN_USB 31
F_TYPEC
F_USB2 x4 HUB

USB 3.1 Gen 2



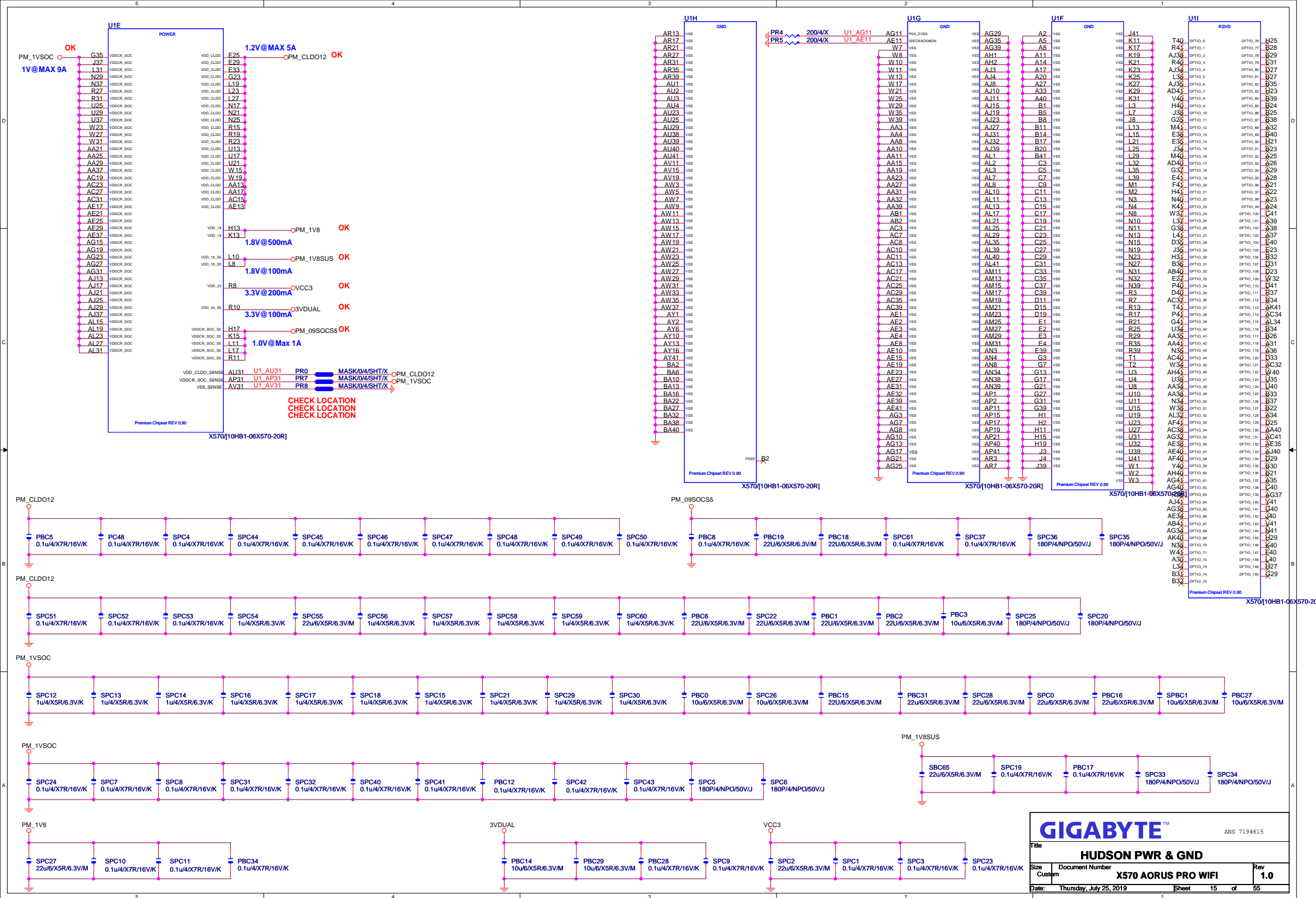
F_TYPE A 2
F_TYPE A 1
R_TYPE C
TYPE A 1G LAN
F_TYPE C

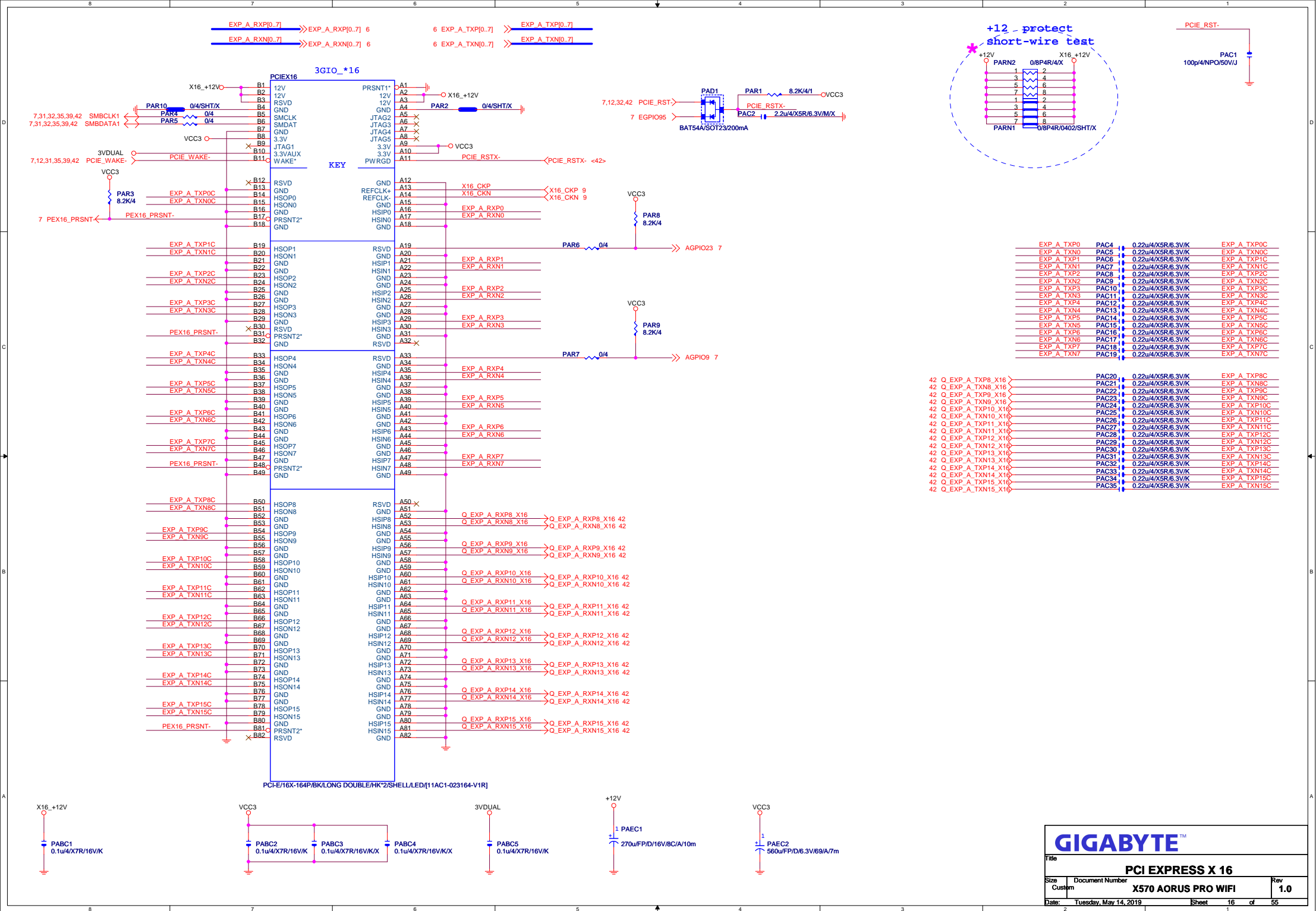
USB Controller 0: USB20 [0:5], USB30 [0:3]
USB Controller 1: USB20 [6:11], USB30 [4:7]

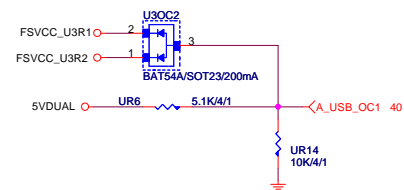
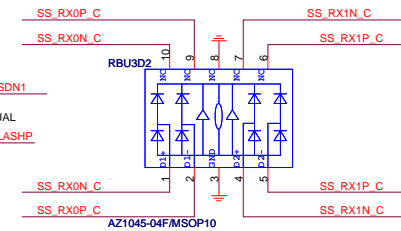
USB Controller 0: USB20 [0:5], USB30 [0:3]
USB Controller 1: USB20 [6:11], USB30 [4:7]

USB30G2	USB20
0	4
1	4
2	5
3	5
4	9
5	8
6	7
7	6

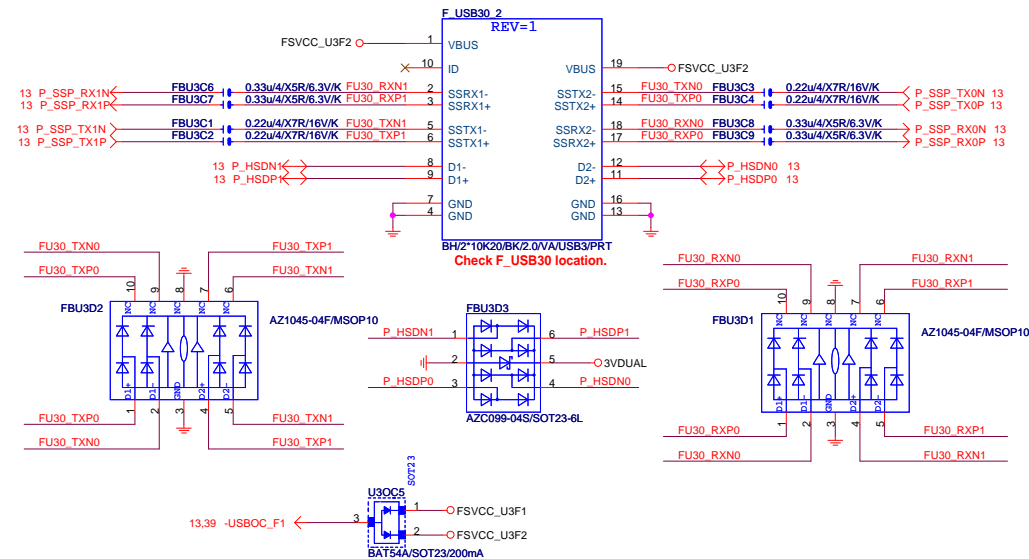
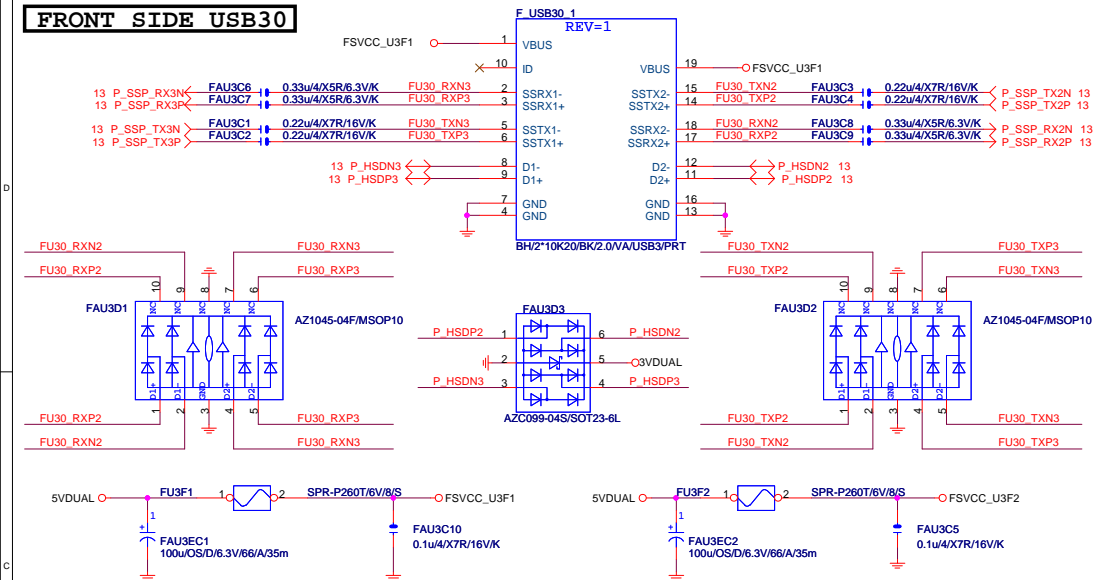
USB30_I211 Lan
F_USB 30_2
F_USB 30_1
FRONT TYPE C



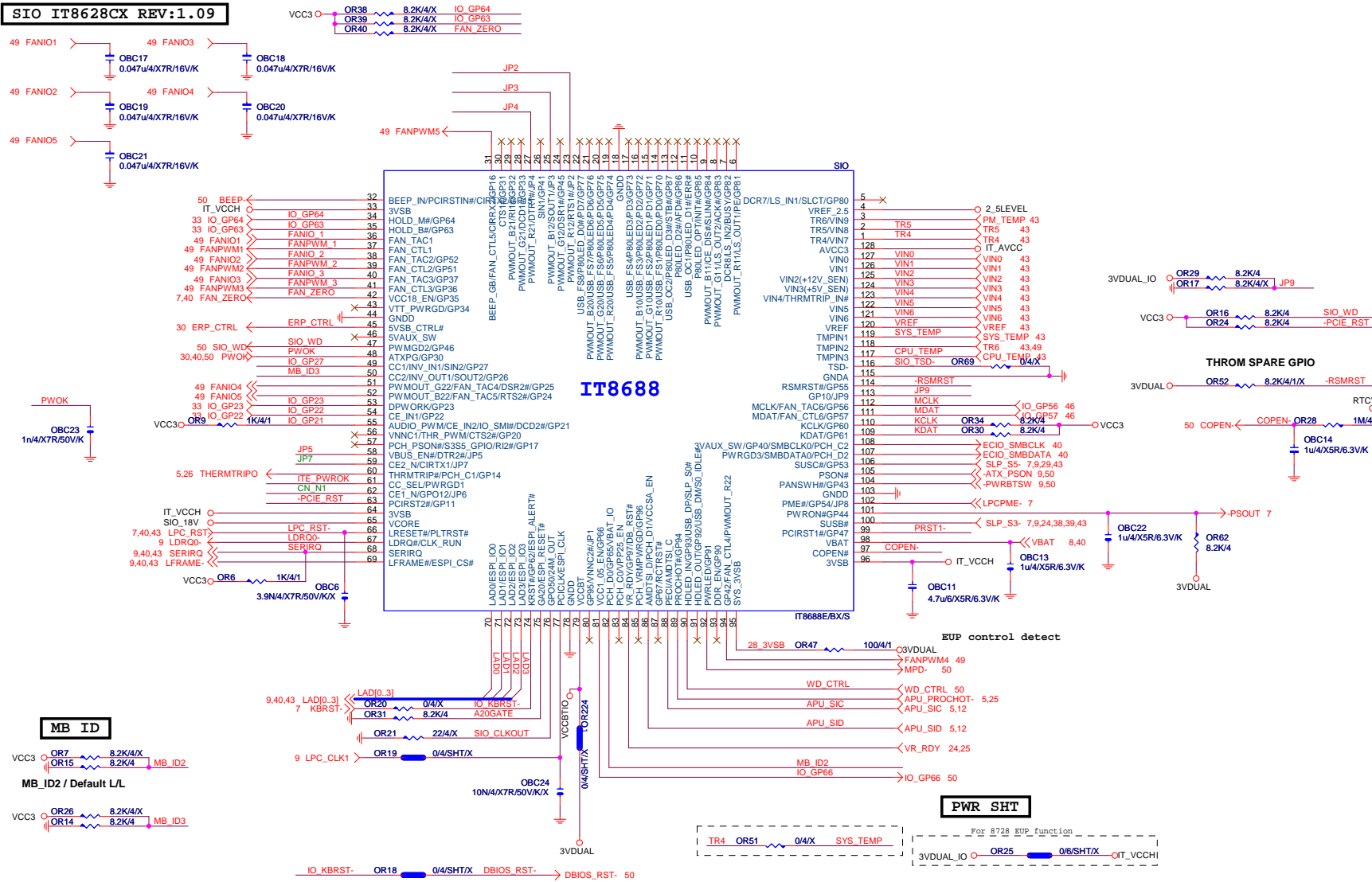




FRONT SIDE USB30



SIO IT8628CX REV:1.09



SIO STRAP



JP2	1	Disable WDT to rest PWROK
	0	Enable WDT to rest PWROK
JP3	1	Dual-BIOS CS pin mode select bit "0"
	0	See the below table
JP4	1	LPC/ESPI power VCCBT = 3.3V
	0	LPC/ESPI power VCCBT = 1.8V
JP5	1	LPC I/F
	0	ESPI I/F
JP6	1	Enable Dual BIOS Function (for GigaByte Only)
	0	Disable Dual BIOS Function (for GigaByte Only)
JP7	1	Dual-BIOS CE pin mode select bit "1"
	0	See the below table
JP7	1 1	CE pin disable (Hold pin mode)
	1 0	CE mode 1
	0 1	CE mode 2
	0 0	CE mode 3

FAN TABLE	
CPU_FAN	FAN_CTL1 FAN_TAC1
SYS_FAN1	FAN_CTL2 FAN_TAC2
SYS_FAN2	FAN_CTL3 FAN_TAC3
SYS_FAN3	FAN_CTL4 FAN_TAC4
OPT_FAN or SYS_FAN4	FAN_CTL5 FAN_TAC5
THRMTRIP	PIN56
PROCHOT	PIN89

DUAL BIOS OPT STRAP

OR58 上件/OR56 不上件 SINGLE BIOS,
OR58 不上件/OR56 上件 DUAL BIOS

EMI

OBC27 0.01u/4X7R/25V/K VCC3

Power leakage

SIO_18V

internal power pin, max 22nF cap
SIO_18V
OBC4 0.1u/4X7R/16V/K
OBC5 0.1u/4X7R/16V/K

SIO CAP

IT_VCC3 OBC12 10u/6X5R/6.3V/M
OBC3 0.1u/4X7R/16V/K
IT_VCC3 OBC2 1u/4X5R/6.3V/K
OBC7 0.1u/4X7R/16V/K
IT_AVCC OBC10 10u/6X5R/6.3V/M
OBC8 0.1u/4X7R/16V/K
2_5LEVEL OBC16 22u/6X5R/6.3V/M/X
2_5LEVEL OBC15 1u/4X5R/6.3V/K/X
CLOSE SIO PIN4 2_5LEVEL

PWR SHT

For 8728 EUP function
3VDUAL_IO OR25 0/6/SHT/X IT_VCC3

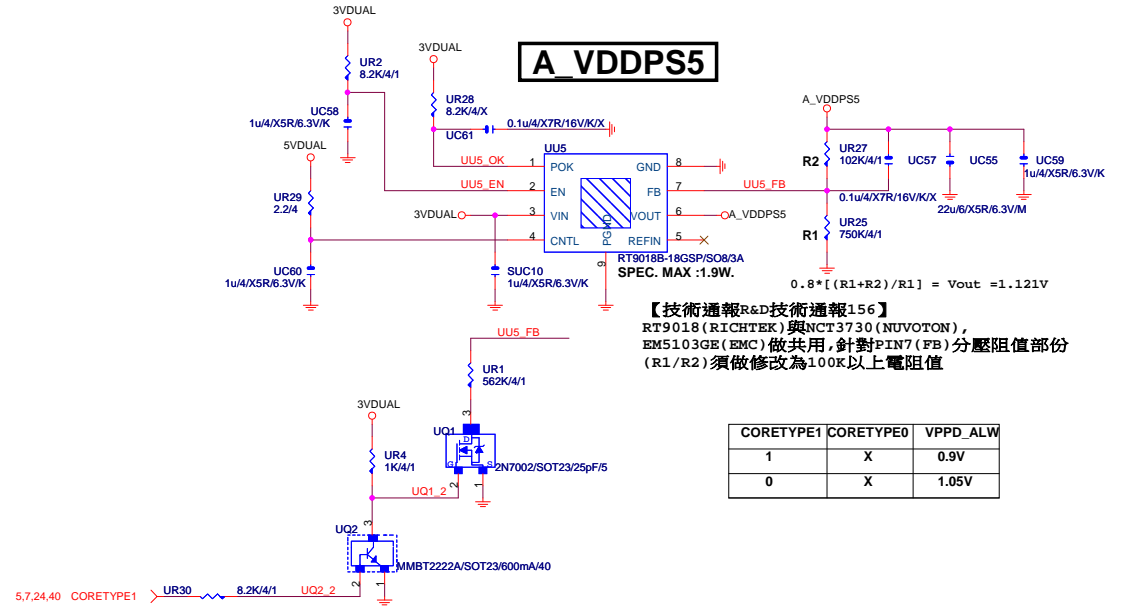
SIO PU

LDRQ0- OR27 1K/4/1 VCC3
PRST1- OR11 1K/4/1 VCC3
ITE_PWROK OR10 1K/4/1 VCC3

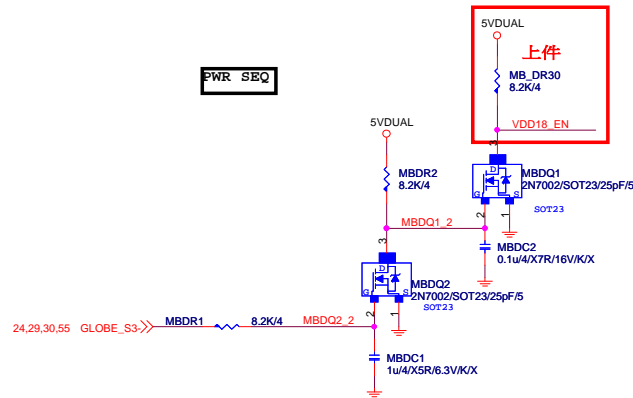
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ITE 8686 LPC IO, TPM, KB/MS		
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A_VDDPS5

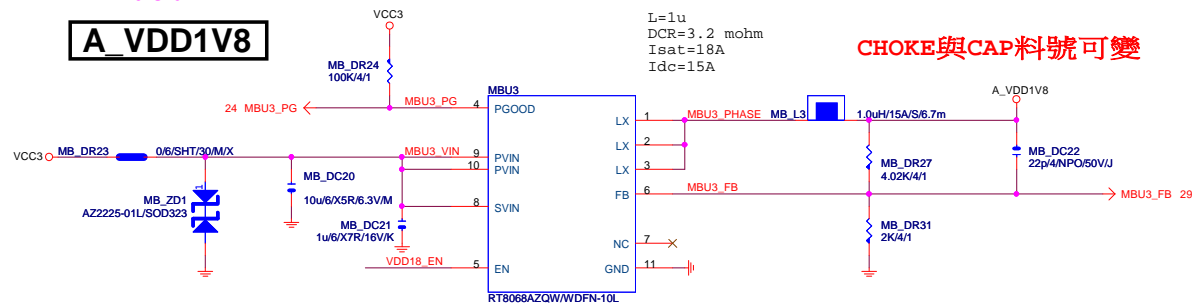


PWR_SEQ

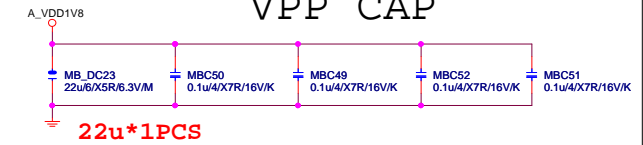


REV:0.4

A_VDD1V8



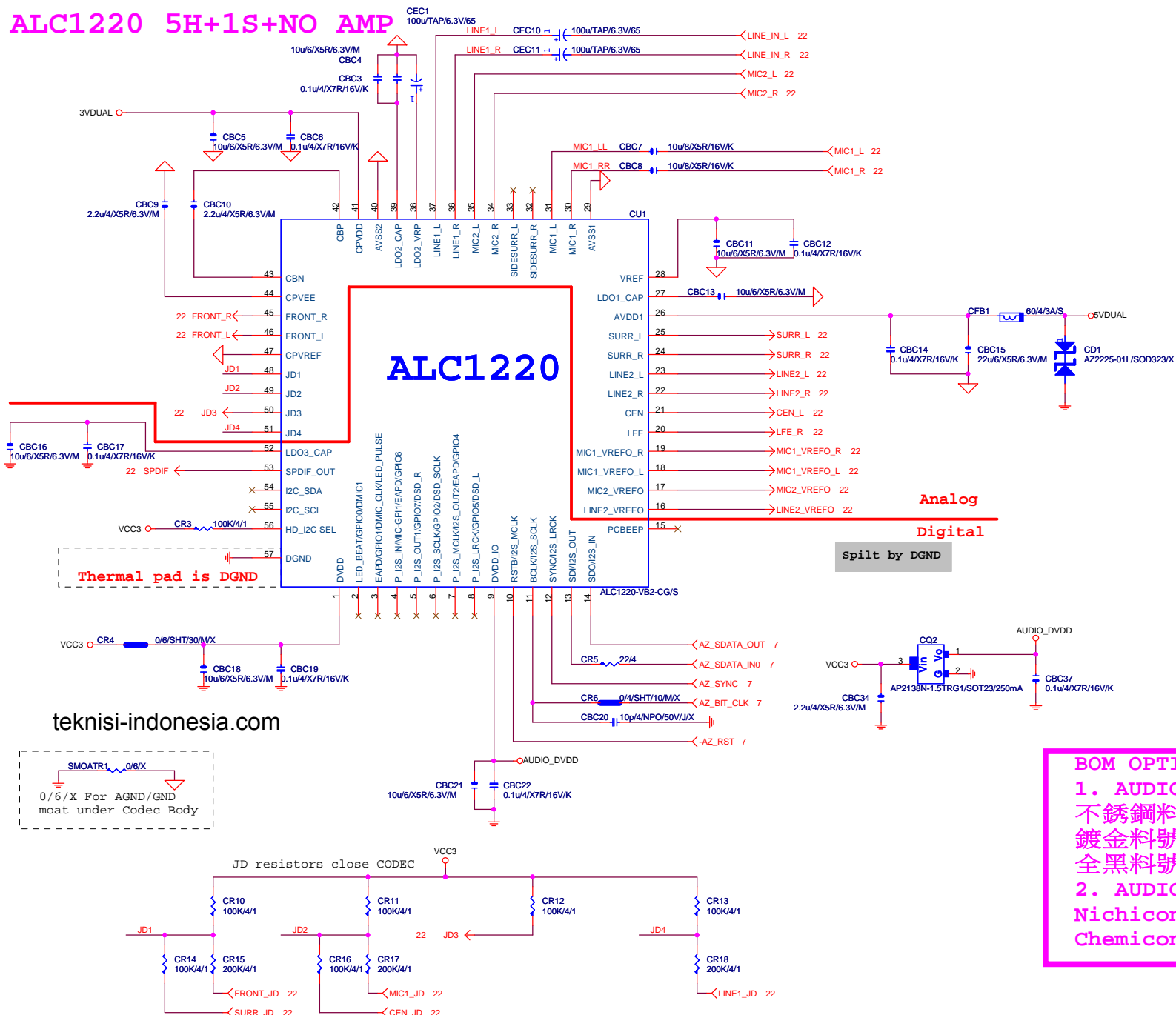
VPP CAP



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Title FAN_A_VDD1V8		
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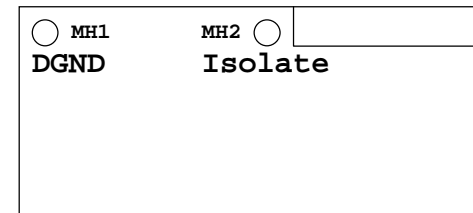
Rev 4.1

ALC1220 5H+1S+NO AMP

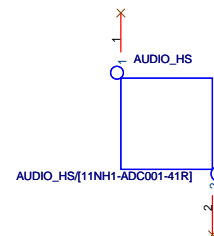
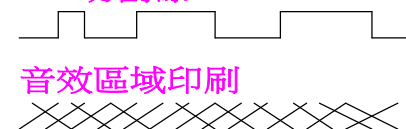


LAYOUT注意: 螺絲孔下GND方式

1. MH1下DGND
2. MH2一律改為Isolate



LAYOUT注意: 是否要加?
AGND切割線



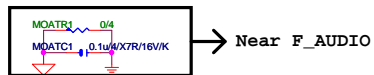
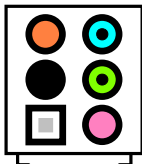
BOM OPTION :

1. AUDIO CONNECT
不銹鋼料號: 11NR6-403025-A3R
鍍金料號: 11NR6-403025-92R
全黑料號: 11NR6-403025-B1R
2. AUDIO CAP
Nichicon MW音效電容 : 100u/TAP/6.3V/65
Chemicon音效電容 : 100uF/TAP/10V/6*5

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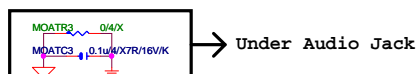
AZALIA JACK



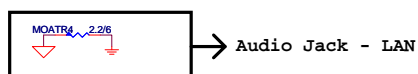
Near F_AUDIO



Near Codec

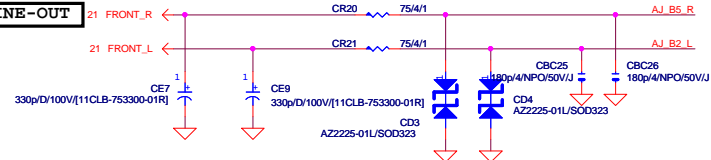


Under Audio Jack

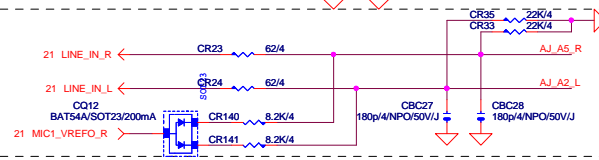


Audio Jack - LAN

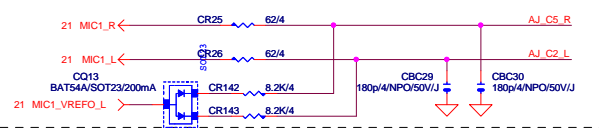
LINE-OUT



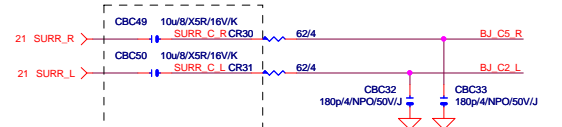
LINE-IN



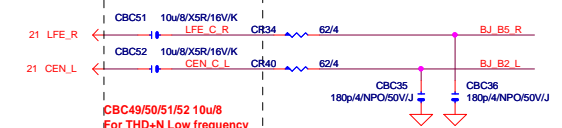
MIC-IN



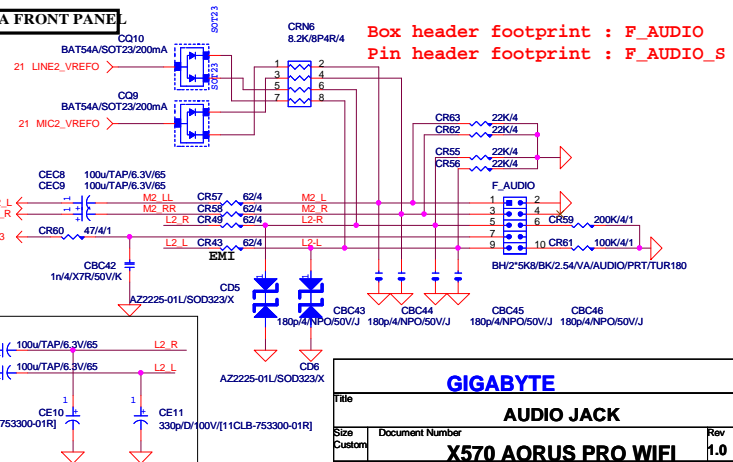
SURROUND



CEN/LFE

CBC49/50/51/52 10u/8
For THD+N Low frequency

AZALIA FRONT PANE

Box header footprint : F_AUDIO
Pin header footprint : F_AUDIO_S

GIGABYTE

AUDIO JACK

File		
X570 AORUS PRO WIFI		
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Custom		1.0
Date		
Tuesday, May 14, 2019		
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AZALIA JACK

BLUE
LINE-INGREEN
LINE-OUTPINK
MIC-IN

可變

5VDUAL

2X3RP/25P/OR,BK,BU,GR,RE,RA[11NR6-403025-B1R]

Orange
CEN/LFEBlack
SURROUND

SPDIF

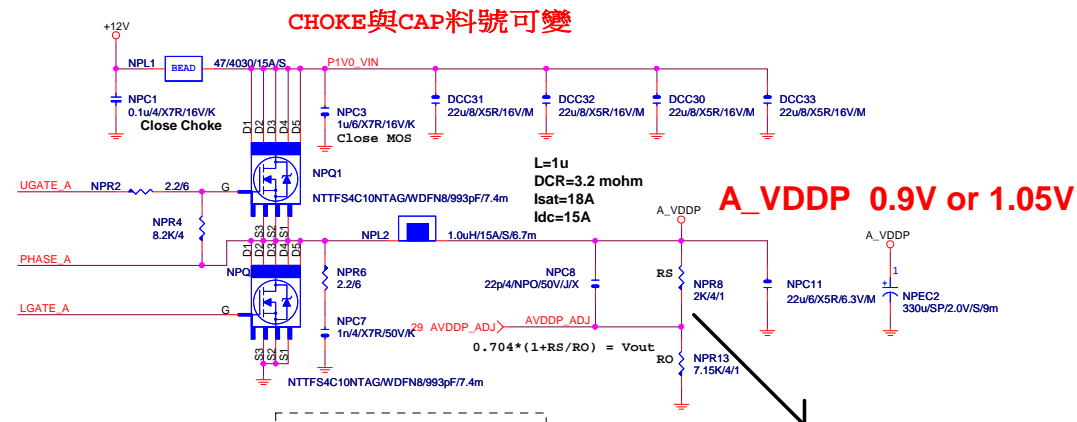
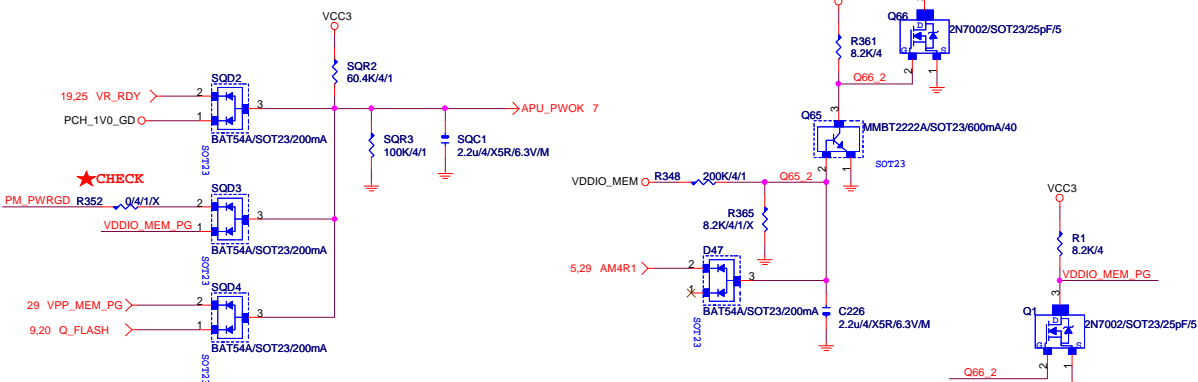
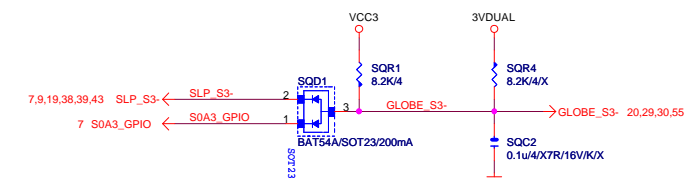
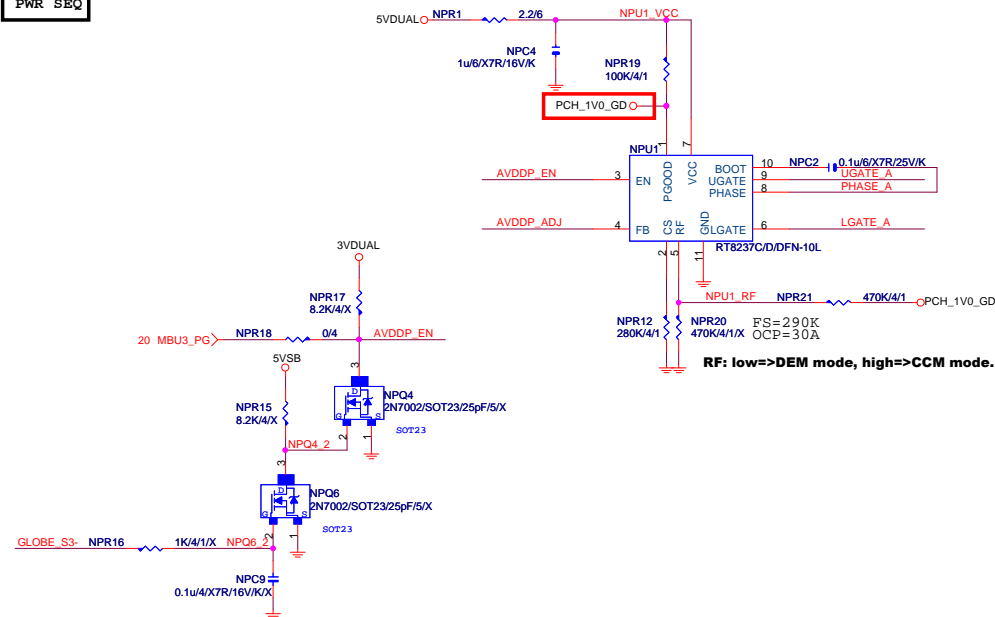
OPTICAL

2X3RP/25P/OR,BK,BU,GR,RE,RA[11NR6-403025-B1R]

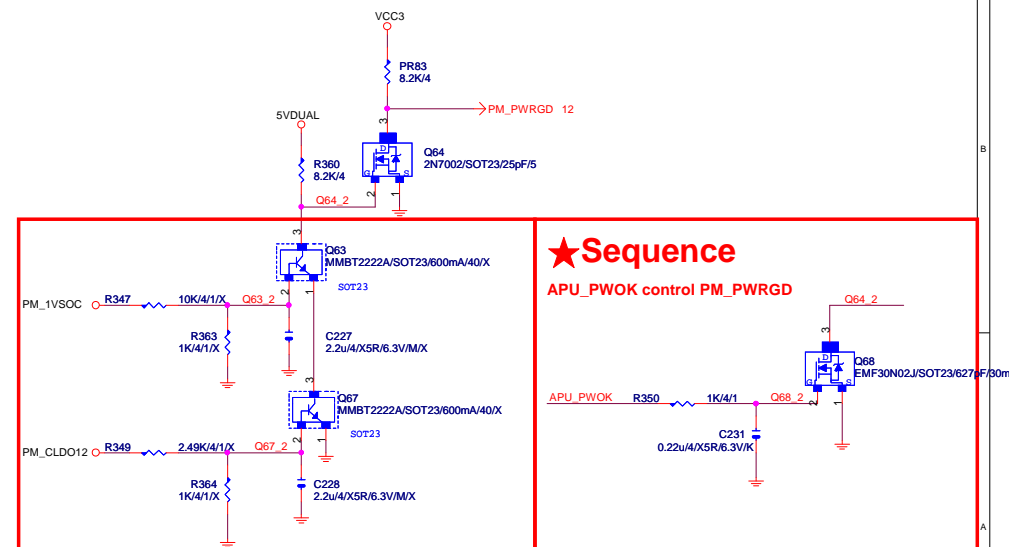
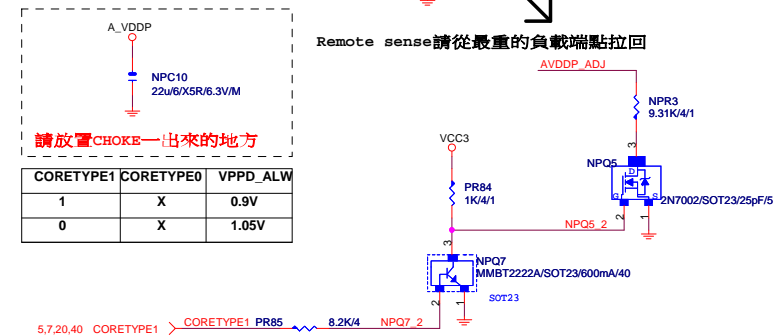
不銹鋼料號:11NR6-403025-A3R
鍍金料號:11NR6-403025-92R
全黑料號:11NR6-403025-B1R

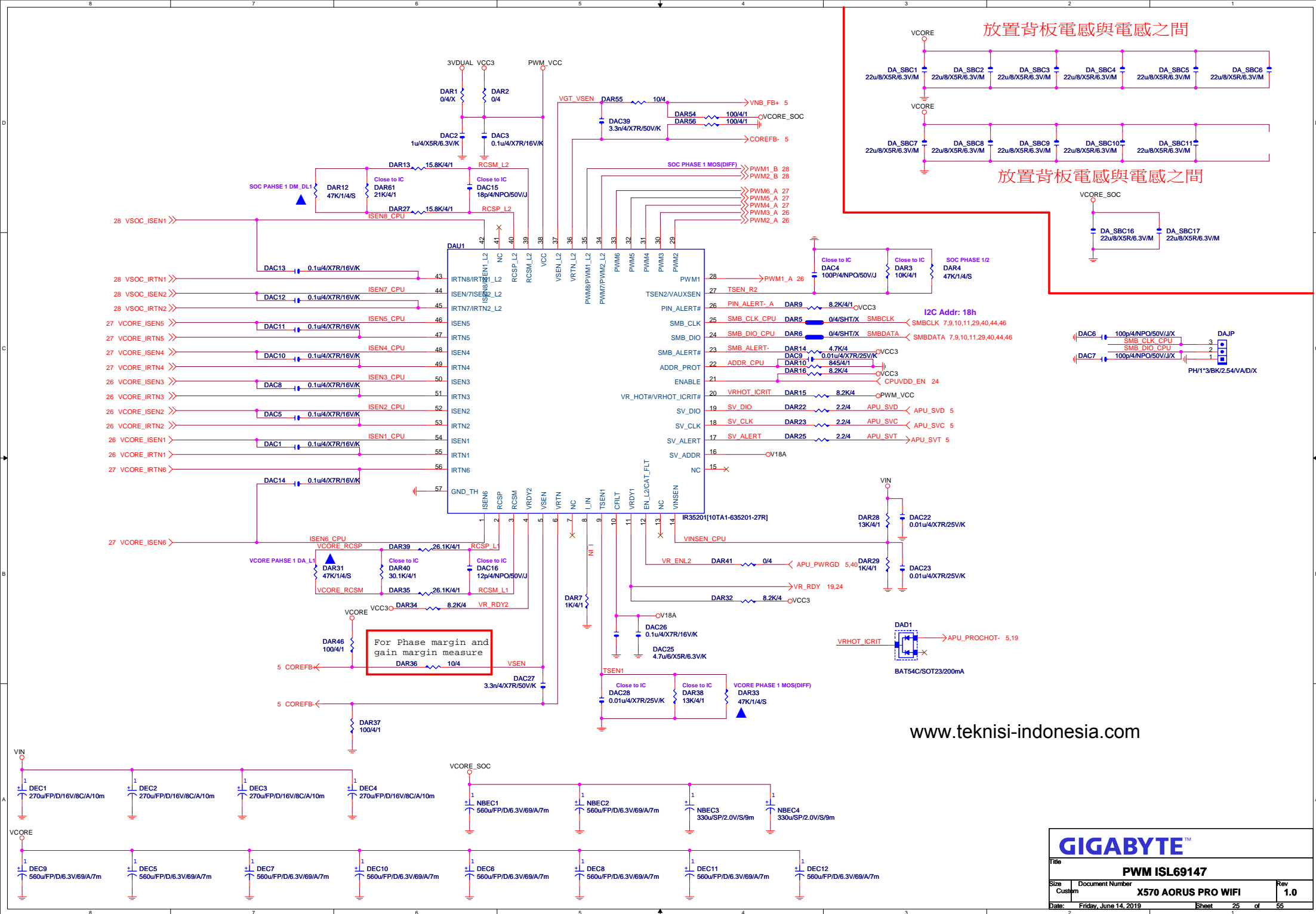
5	4	3	2	1
D				D
C				C
B				B
A				A

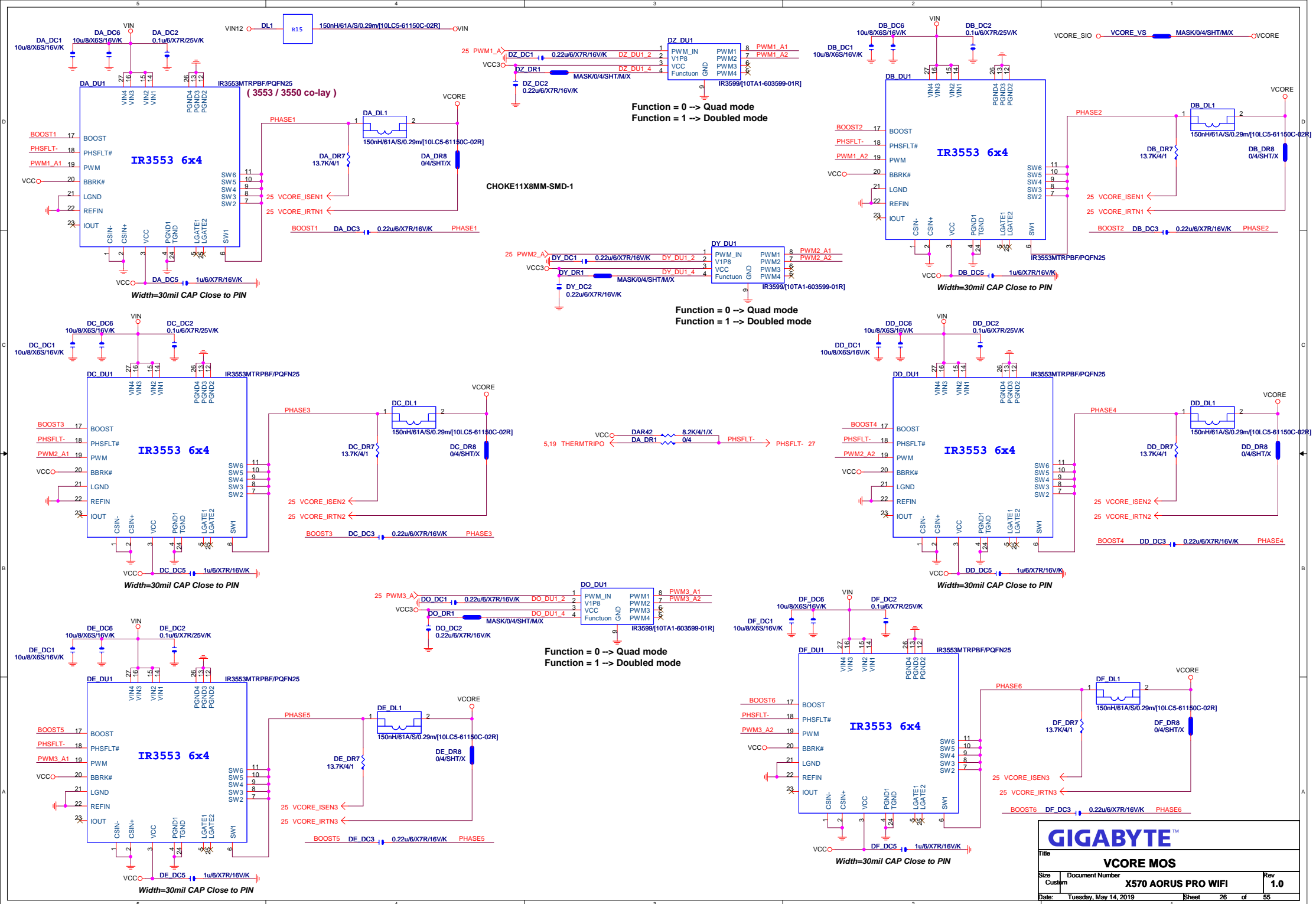
GIGABYTE™		
Title DIGITAL LED		
Size Custom	Document Number X570 AORUS PRO WIFI	Rev 1.0
Date: Tuesday, May 14, 2019	Sheet 23	of 55

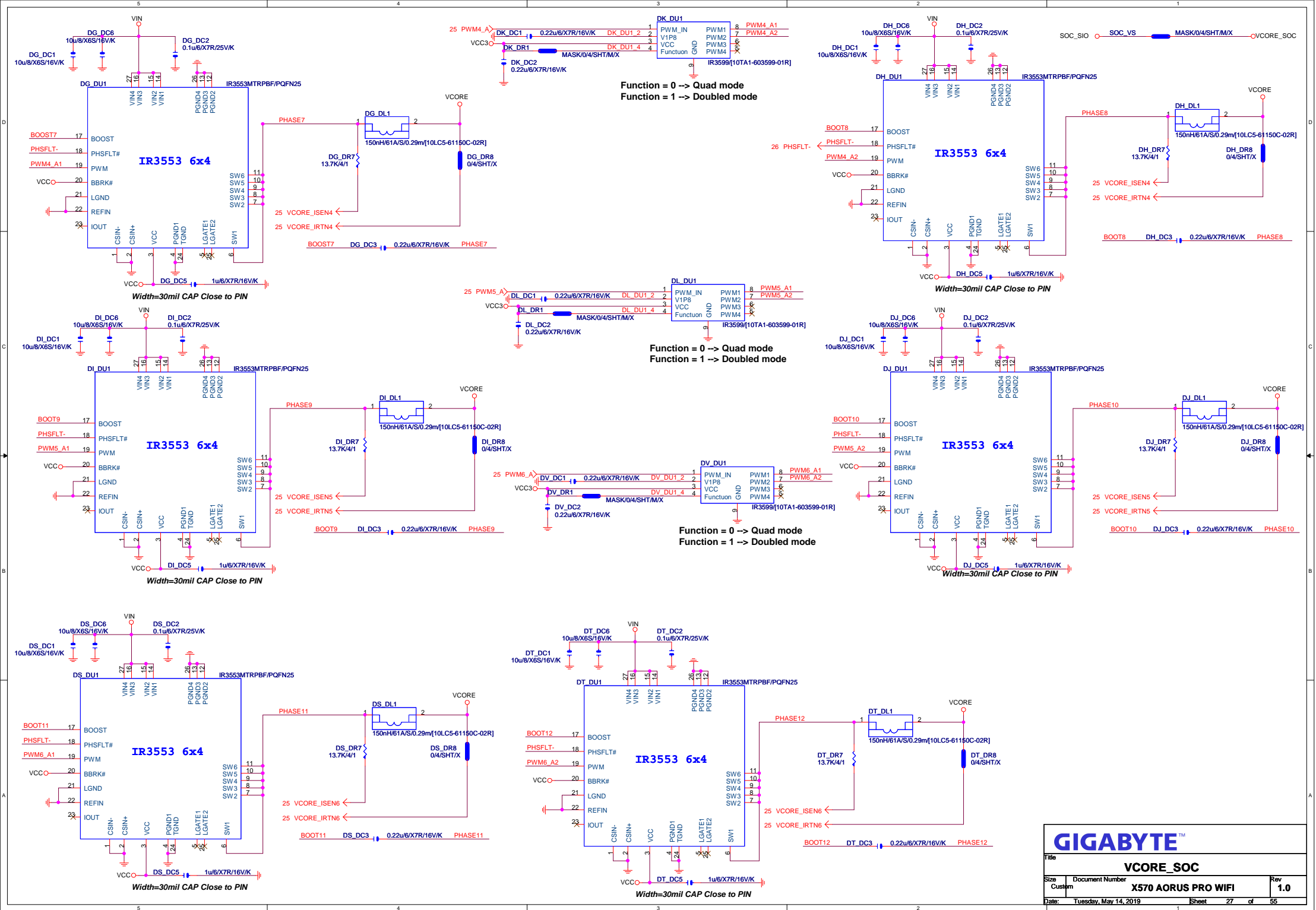


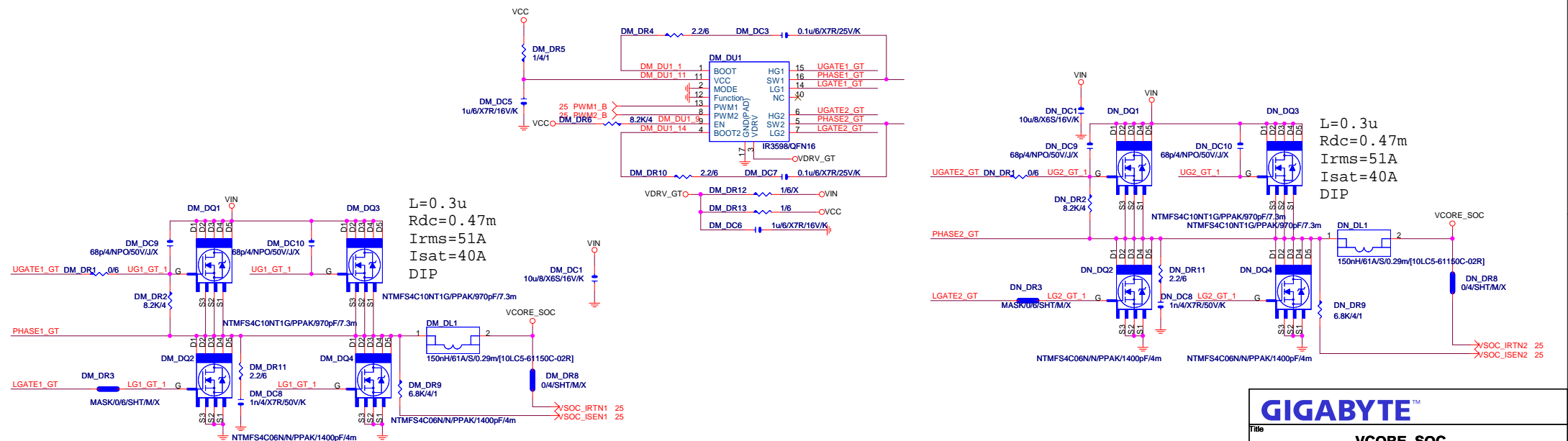
CORETYPE1	CORETYPE0	VPPD_AL
1	X	0.9V
0	X	1.05V







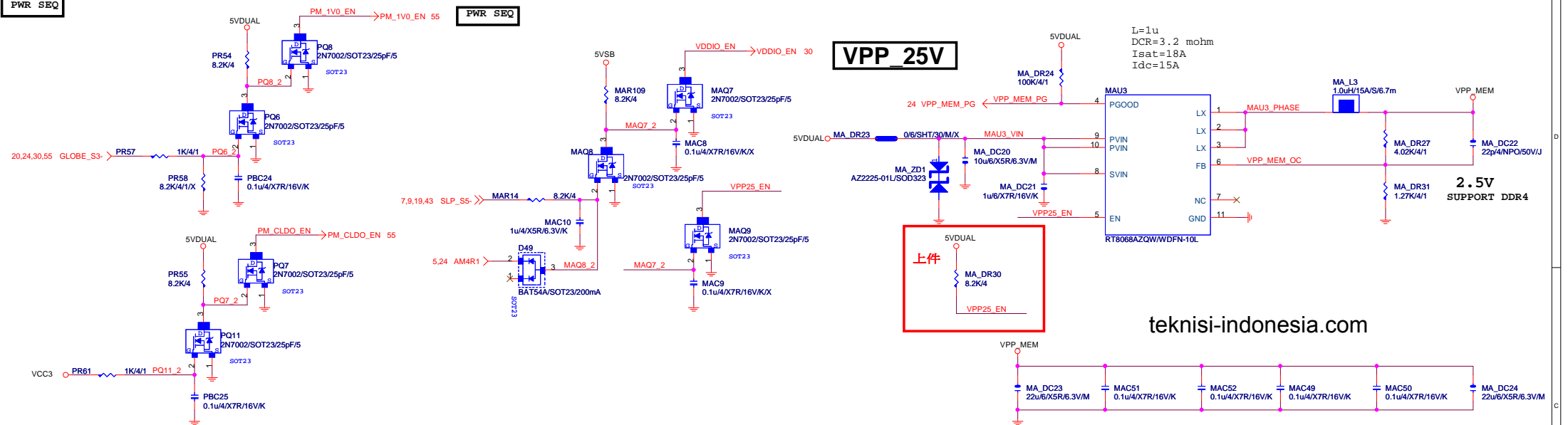




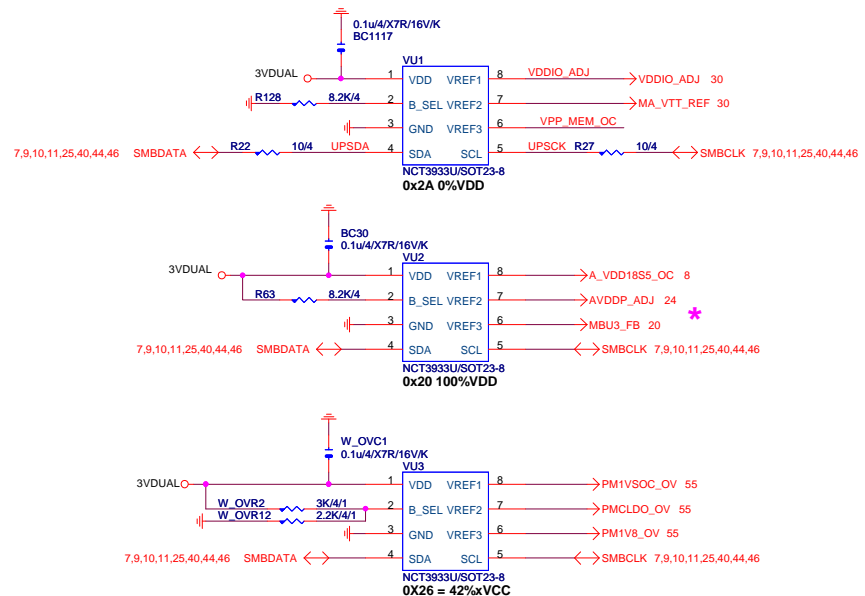
PWR_SEQ

PWR_SEQ

VPP_25V



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Address	0x2A	0x28	0x26	0x24	0x22	0x20
R1 (kΩ)	open	3.9	3	2.2	1.3	10
R2 (kΩ)	10	1.3	2.2	3	3.9	open
ADD_SEL Voltage (% of VCC)	0	25	42	58	75	100

Table1. Recommended Slave Address Setting

GIGABYTE™

Title

PM POWER

Size Custom

Document Number

X570 AORUS PRO WIFI

Rev

1.0

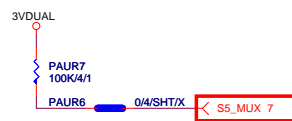
Date: Tuesday, May 14, 2019

Sheet

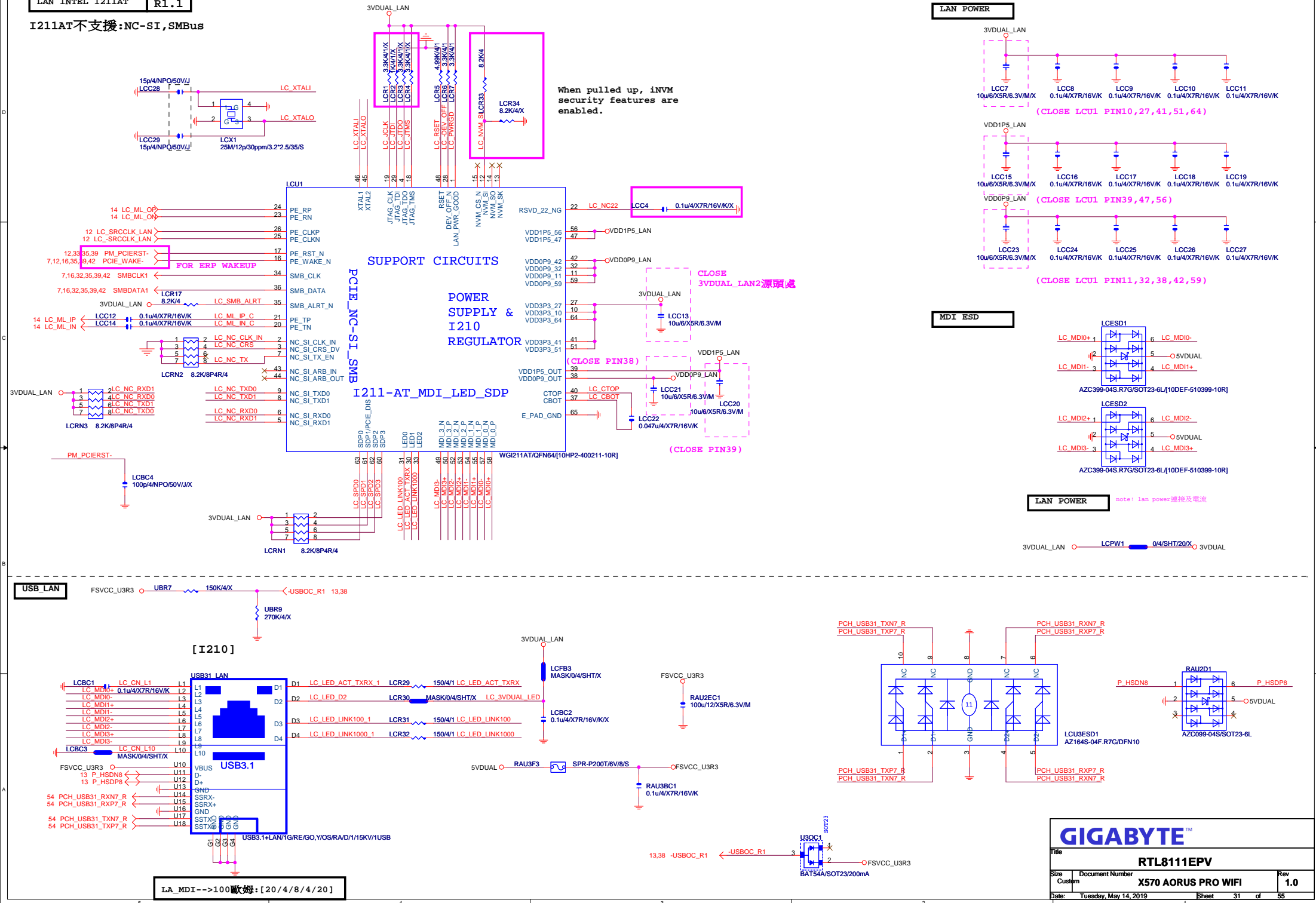
29

of

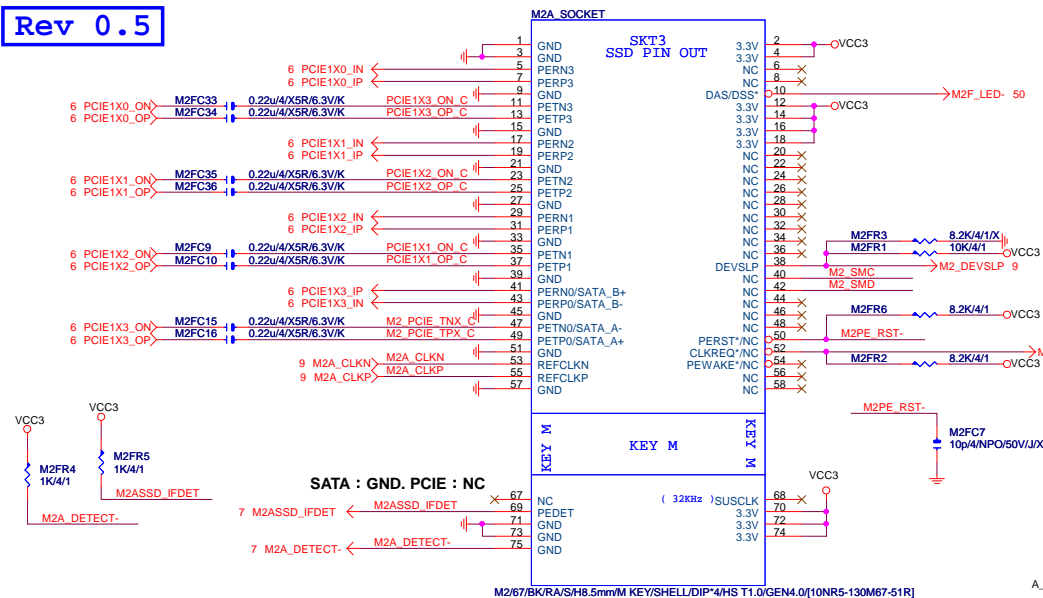
55



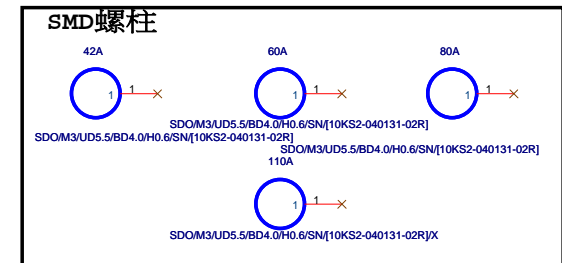
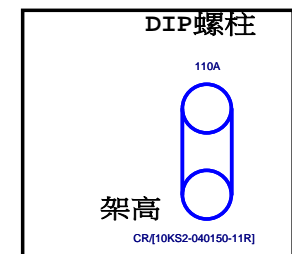
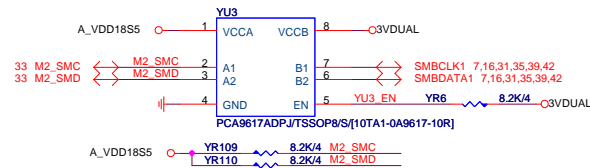
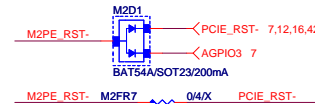
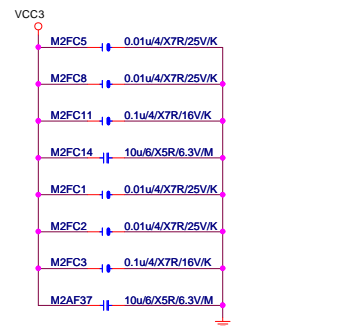
I211AT不支援:NC-SI,SMBus



Rev 0.5



8.5H鐵殼
Footprint :M2_110(DIP)



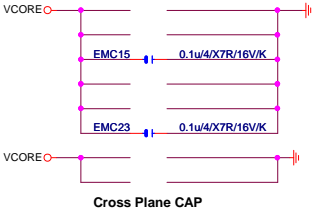
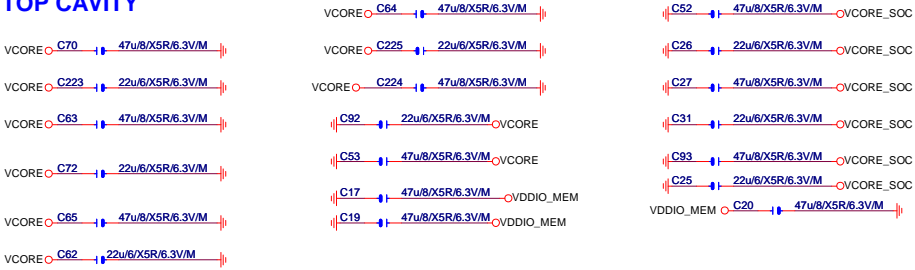
M2A_SOCKET
M2A_SOCKET_HS/[12SP1-S10205-H1R_12SP1-S10205-H3R]

M2 HEATSINK

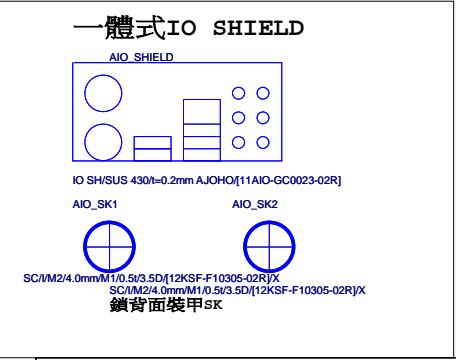
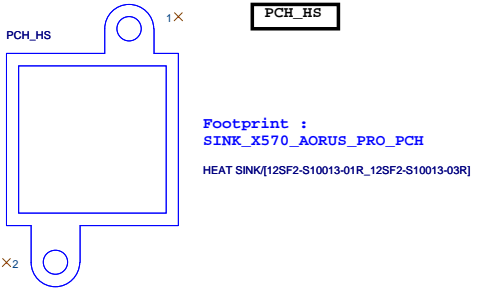
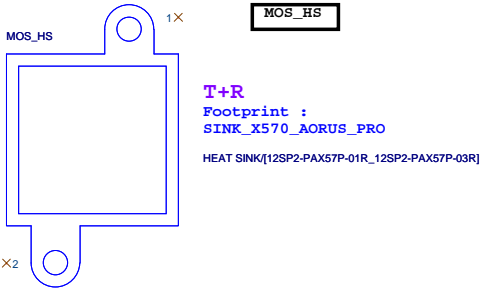
GIGABYTE™

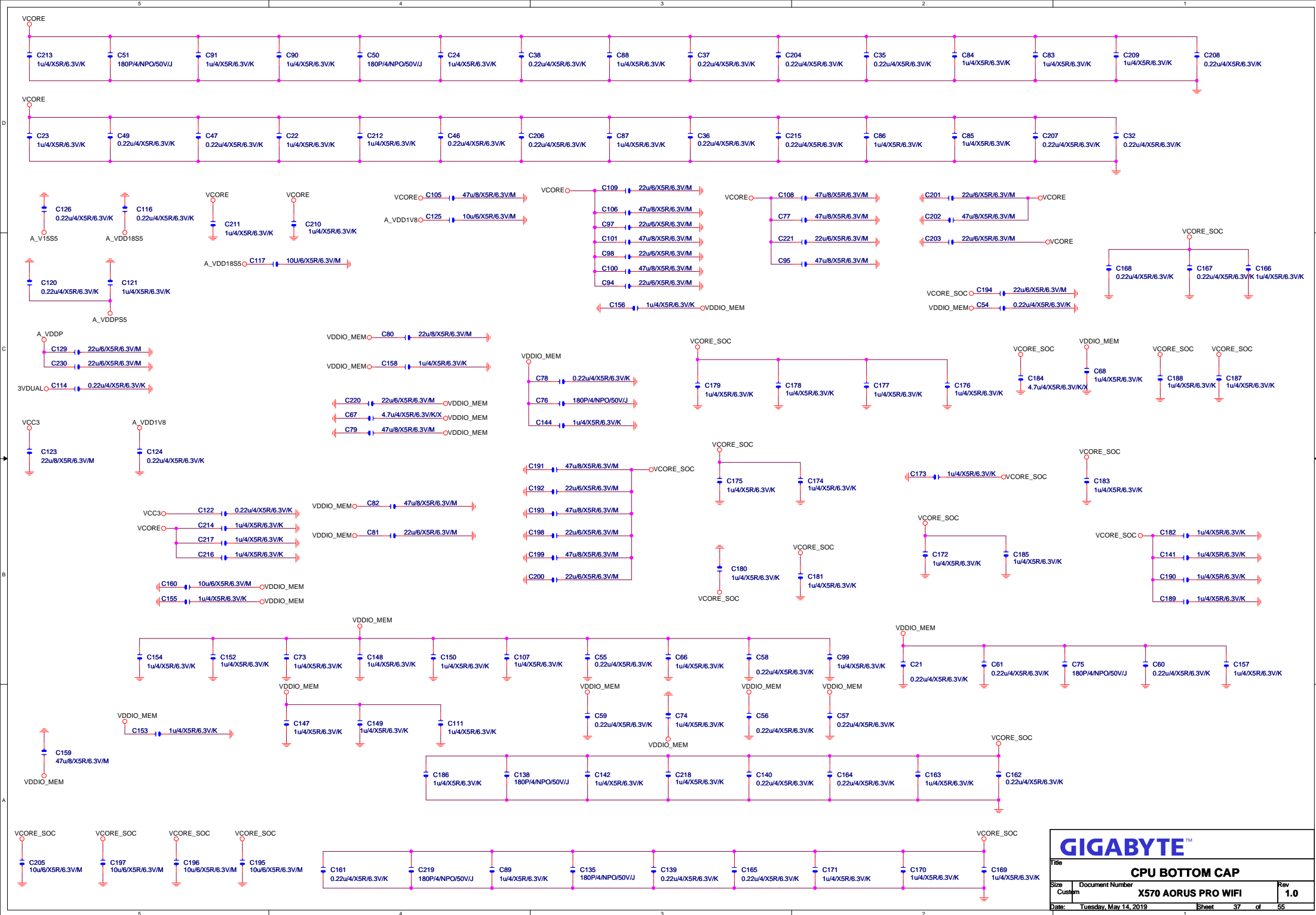
Title			KB_USB_DEC PWR		
Size	Document Number				Rev
Custom	X570 AORUS PRO WIFI				1.0
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CPU TOP CAVITY

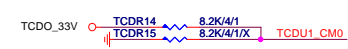
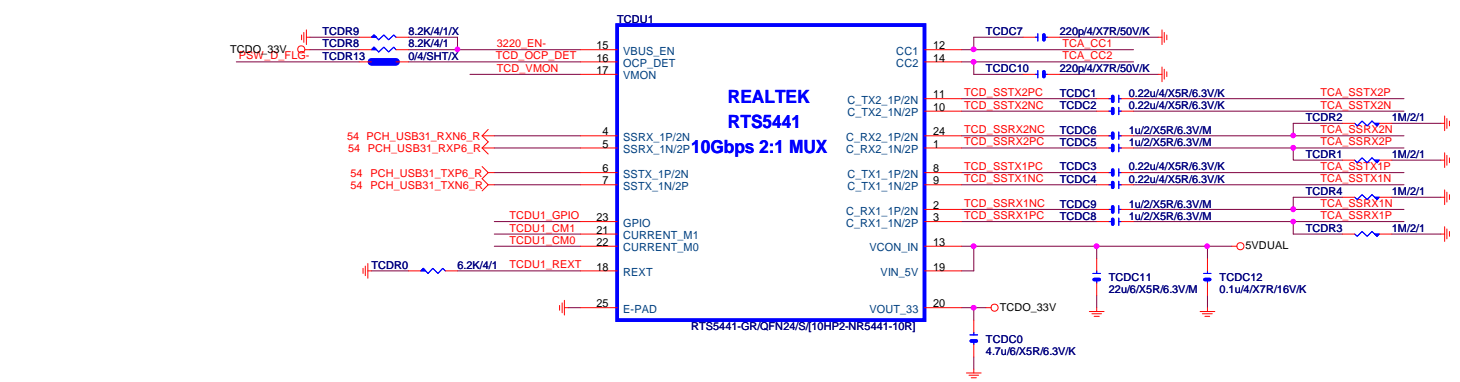


裝甲HEATSINK 分成四大部份

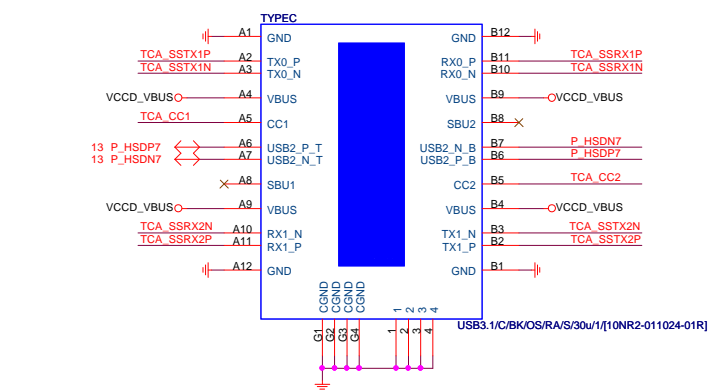
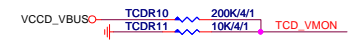
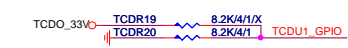




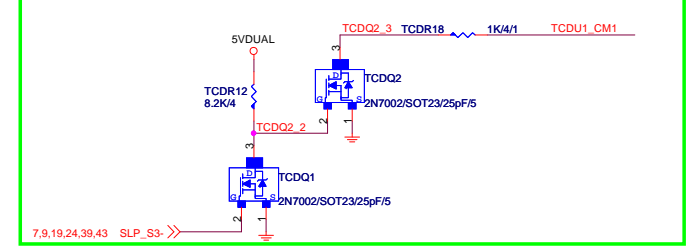
REALTEK CC LOGIC AND MUX RTS5441



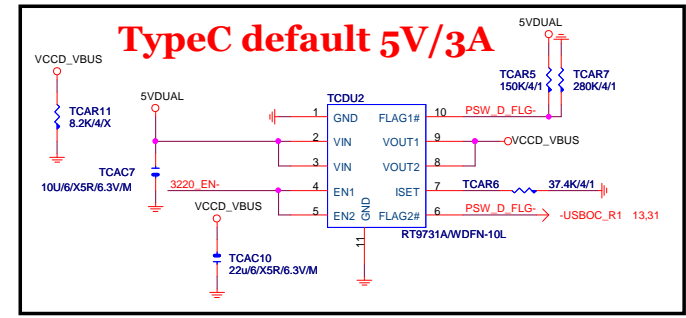
CM1	CM0	Current
0	1	900mA
1	0	1.5A
1	1	3A Default



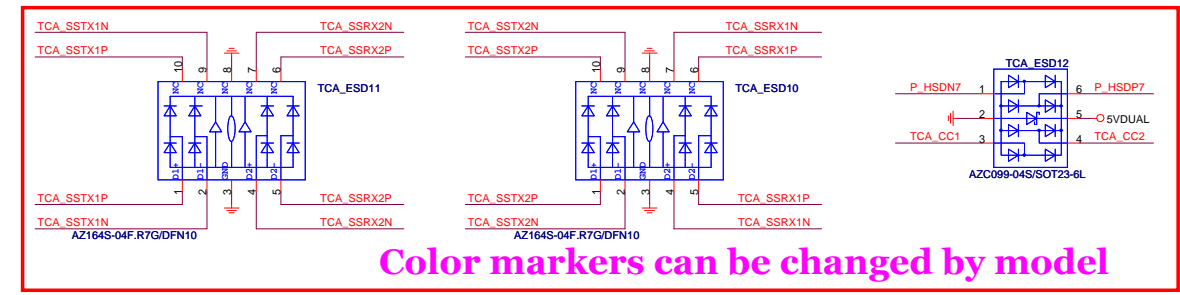
USB2.0 can be used the same source



For VBUS current limit at 900mA on S3



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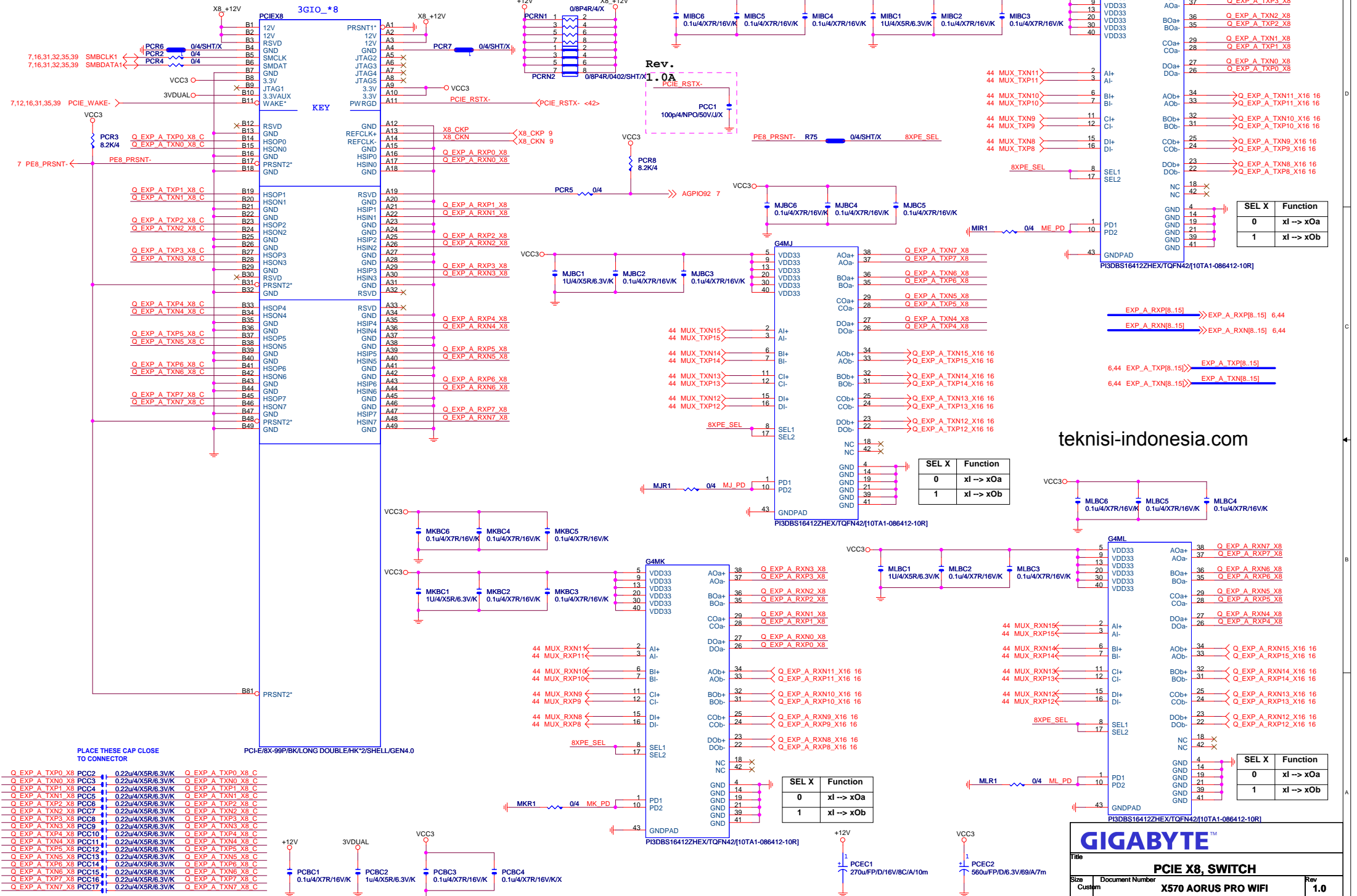
Color markers can be changed by model

5	4	3	2	1
D				D
C				C
B				B
A				A
5	4	3	2	

GIGABYTE™		
Title M.2 SOCKET		
Size Custom	Document Number X570 AORUS PRO WIFI	Rev 1.0
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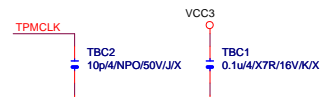
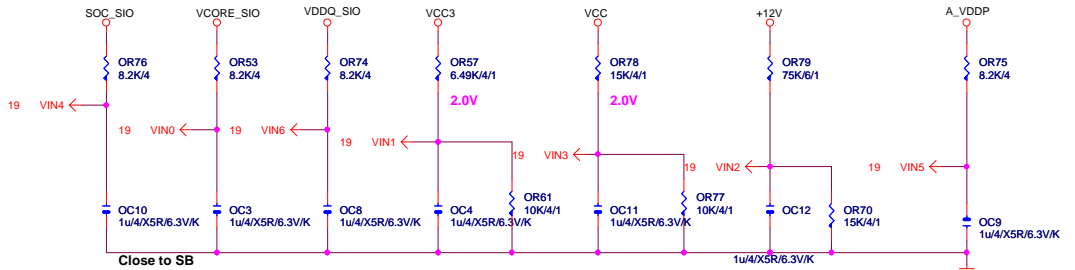
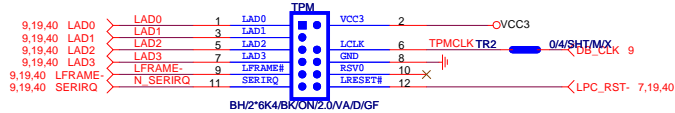
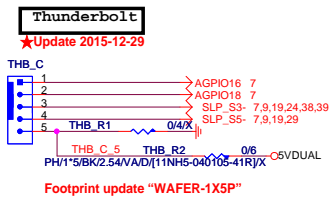
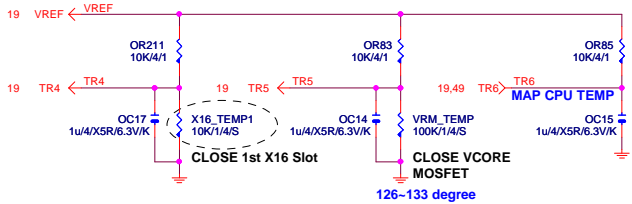
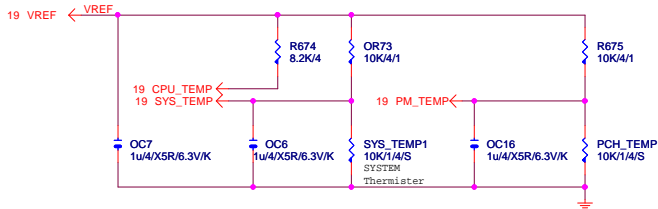
PCIEX8 PROTECT SHT

+12V protect short-wire test



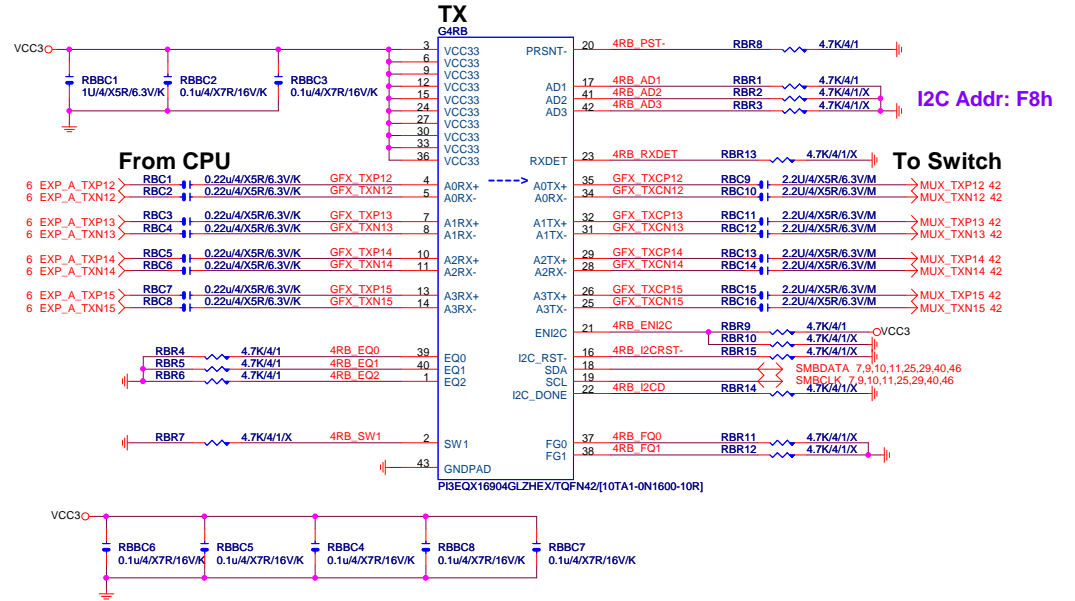
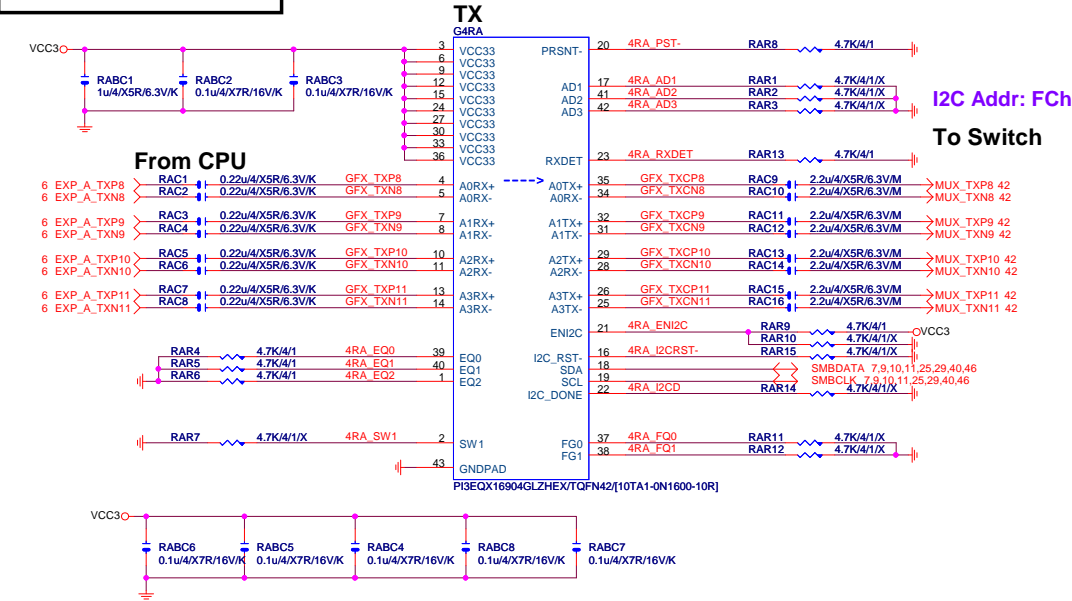
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GIGABYTE
Title: **PCIE X8, SWITCH**
Size: Custom Document Number: **X570 AORUS PRO WIFI** Rev: **1.0**
Date: Tuesday, May 14, 2019 Sheet: 42 of 55

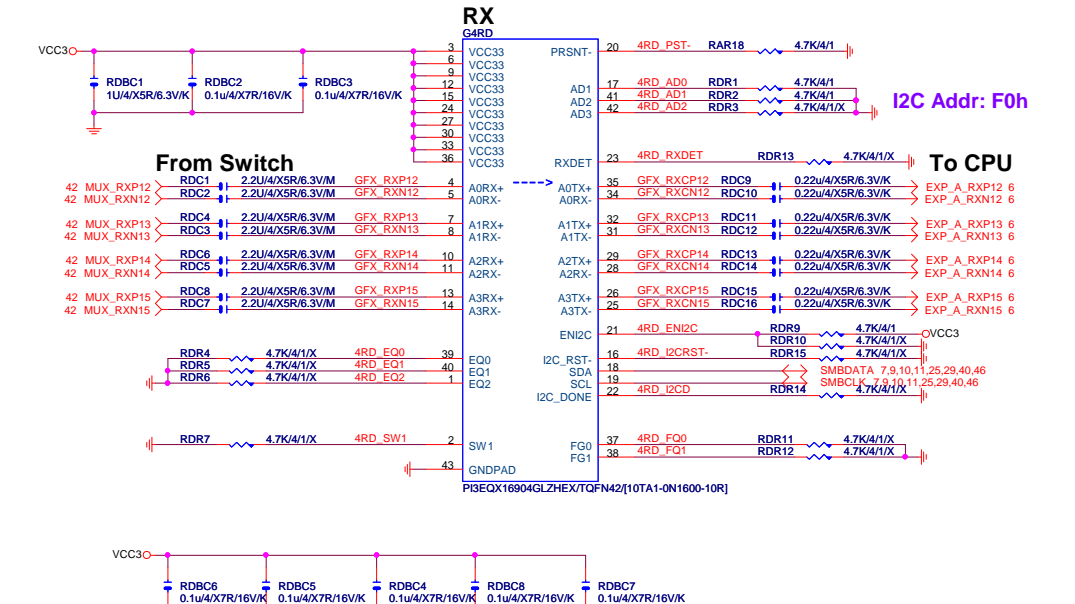
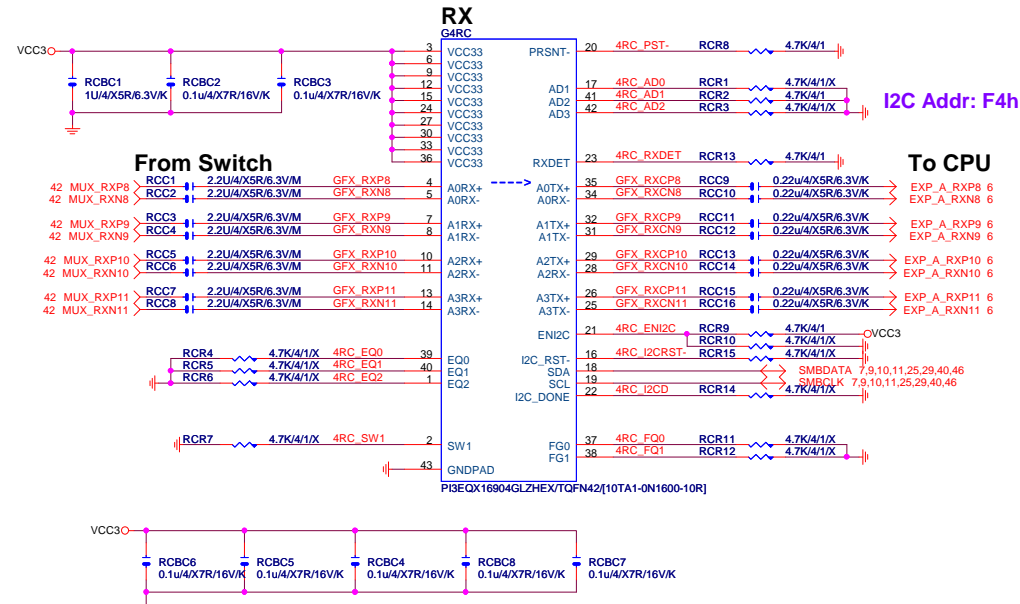


GIGABYTE™			
PCIE X8, SWITCH			
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PCIe GEN 4 REDRIVER



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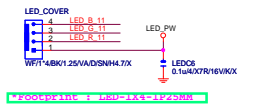
Title			ASM 2142 U31		
Size	Document Number		Rev		
Custom	X570 AORUS PRO WIFI		1.0		
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第一區 LED

第一區 LED

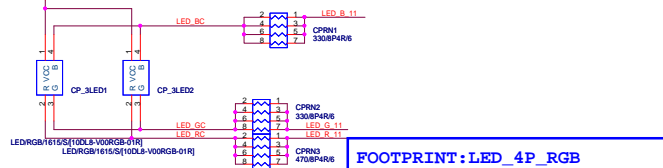
FOR 後窗裝甲 led connect
(LED_COVER放在後窗裝甲內)

(For X570 AORUS ULTRA)

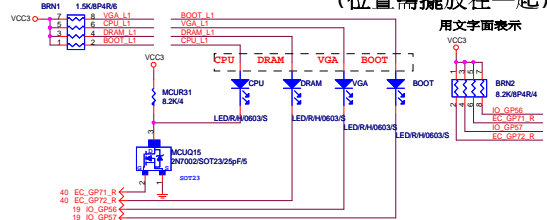


FOR 裝甲高亮度 正發光 LED*2 (位置在正板,依據裝甲設計擺放)

(For X570 AORUS PRO)



FOOTPRINT:LED_4P_RGB

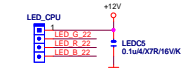
DEBUG PORT *4
(位置需擺放在一起)

用文字面表示

N_GPP_A17	CPU DEBUG
N_GPP_A18	DDR DEBUG
N_GPP_A19	VGA DEBUG
N_GPP_A20	BOOT DEVICE DEBUG
N_GPP_A21	XMP LED SWITCH
N_GPP_A22	TURBO LED SWITCH
N_GPP_D12	LED_IO LED SWITCH

第二區 LED

燈條 LED (LED_CPU放在CPU附近位置)

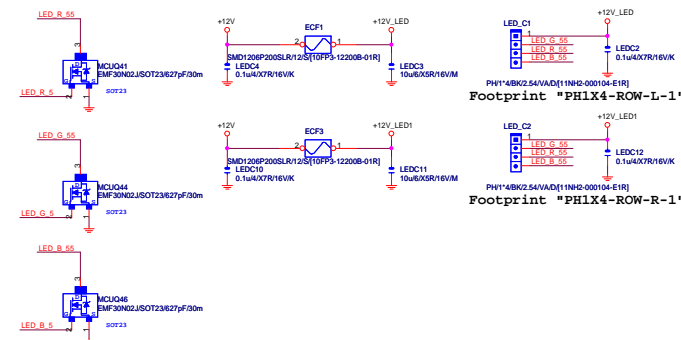


Footprint "PH1X4-FAN-AMD-L"

第五區 LED

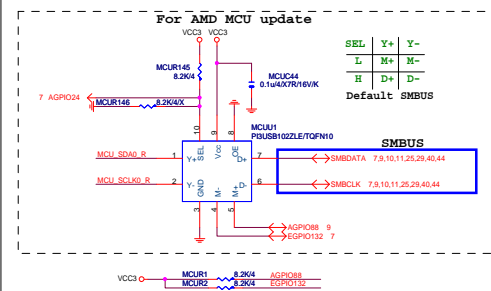
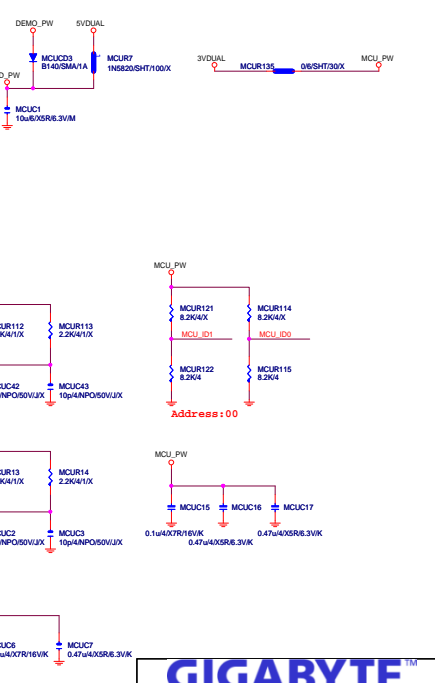
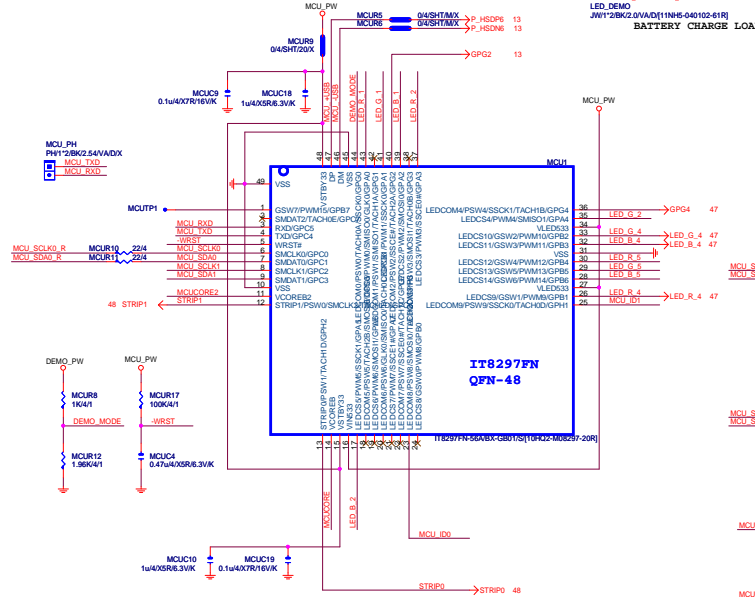
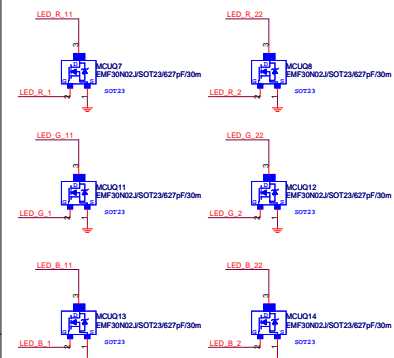
燈條 LED (LED_C1放在PCB左邊板邊位置)

燈條 LED (LED_C2放在PCB右邊板邊位置)



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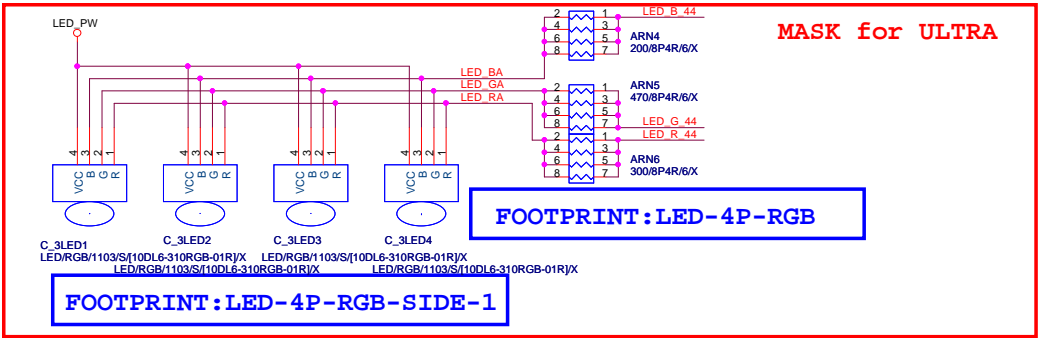
第一區 LED CONTROL 第二區 LED CONTROL



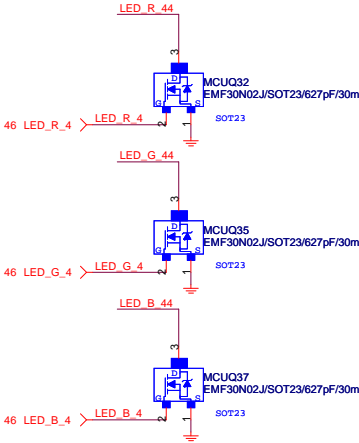
第三區 LED

第四區 LED

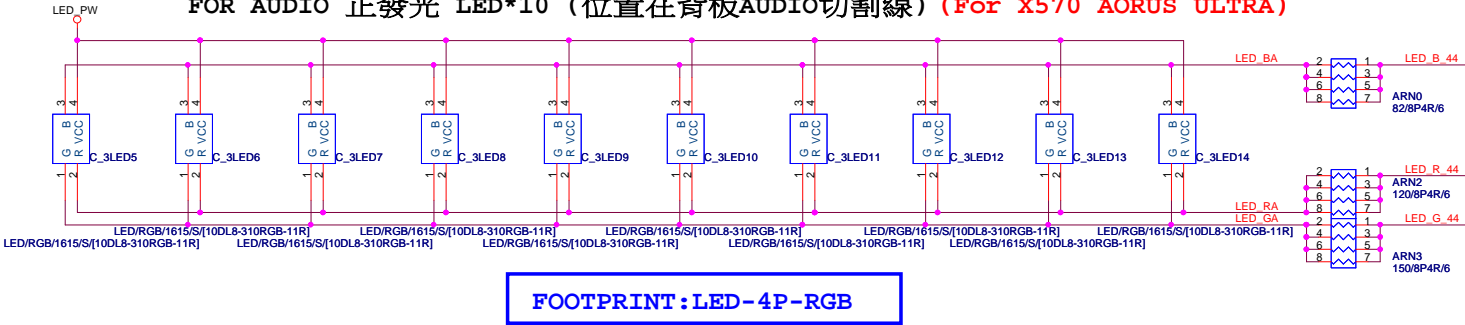
FOR AUDIO 測發光 LED*6 (位置在正板,依據AUDIO_HS設計擺放)
(For X570 AORUS PRO)



第四區 LED CONTROL



FOR AUDIO 正發光 LED*10 (位置在背板AUDIO切割線) (For X570 AORUS ULTRA)

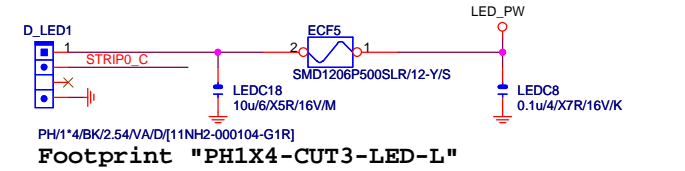


GIGABYTE™			
MODEL / PCB / AUDIO/PCIE LED			
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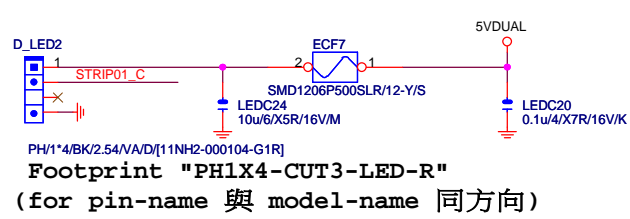
第六區 LED (靠近左上板邊位置)

(靠近右下CPU板邊位置)

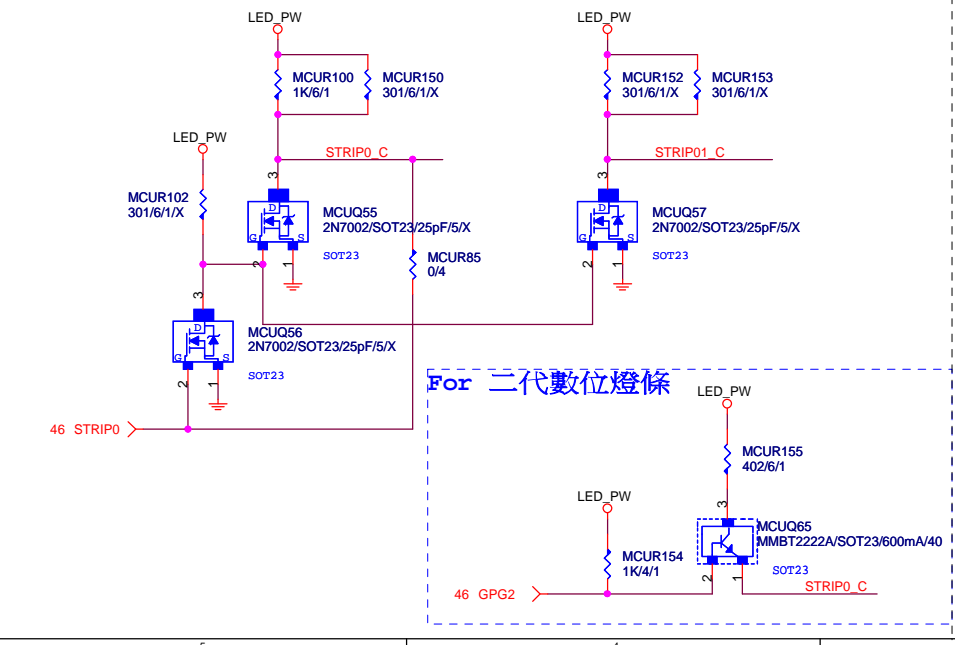
Digital LED Strip1



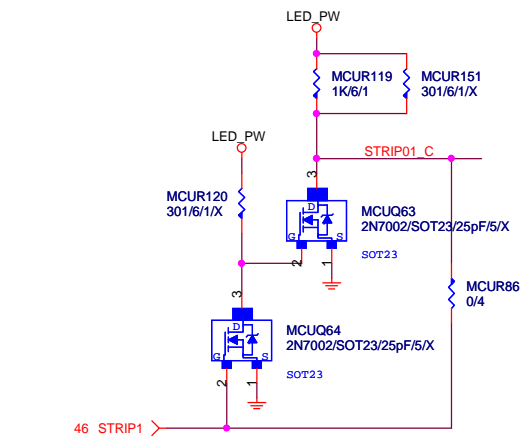
Digital LED Strip2



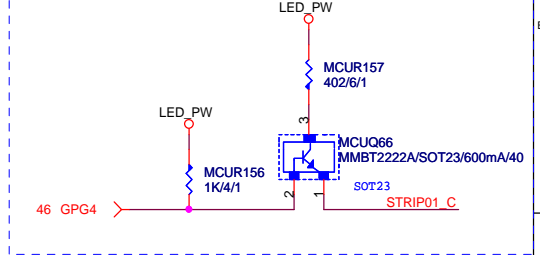
燈條 Level shift

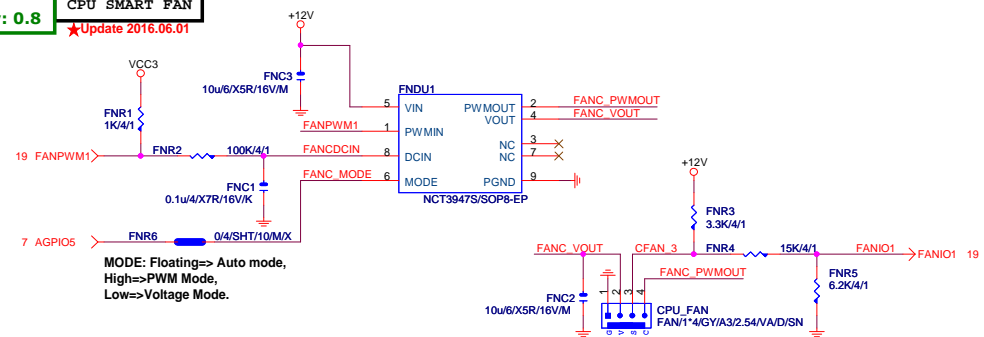


燈條 Level shift

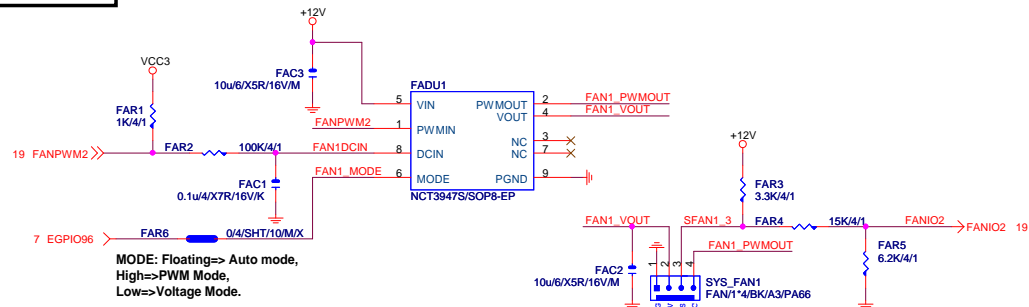


For 二代數位燈條

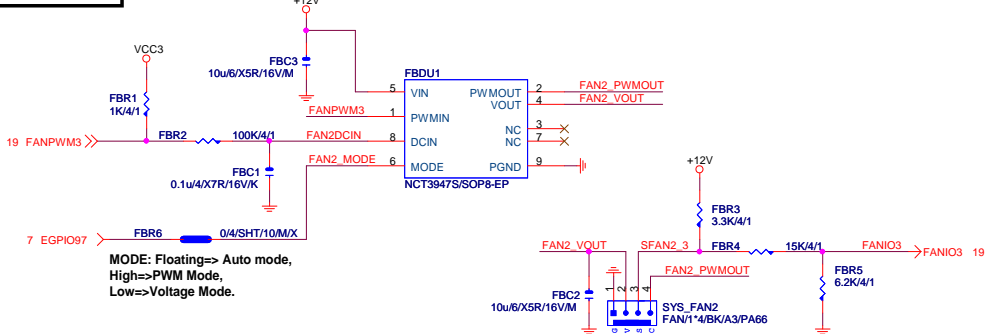




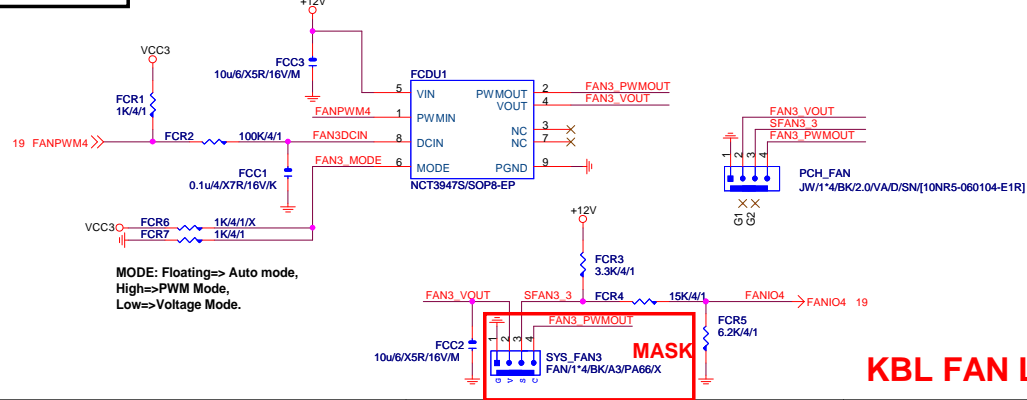
SYSTEM FAN1



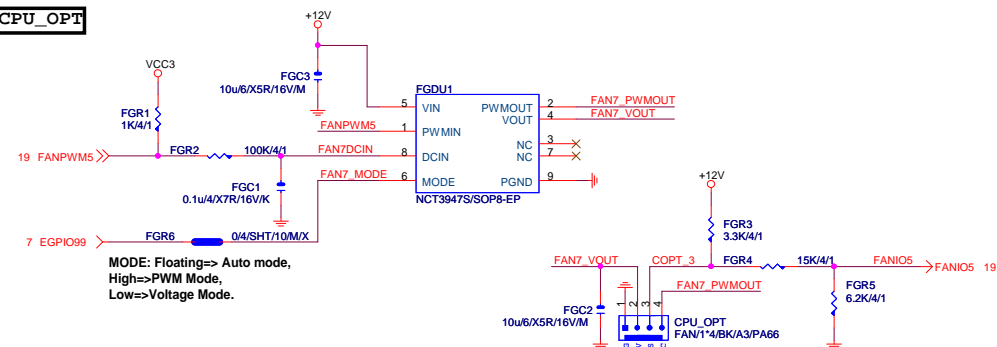
SYSTEM FAN2



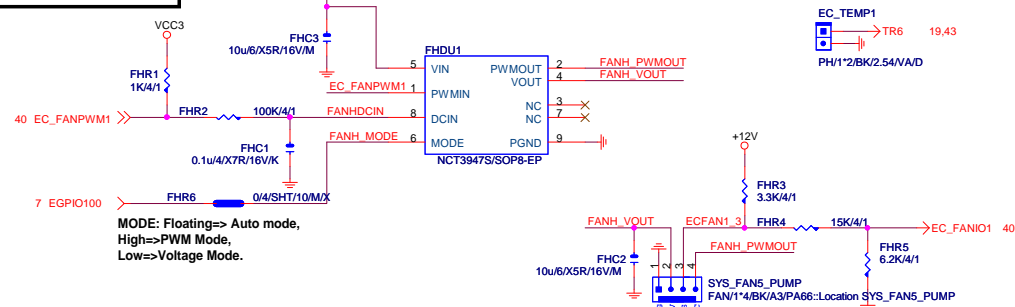
SYSTEM FAN3



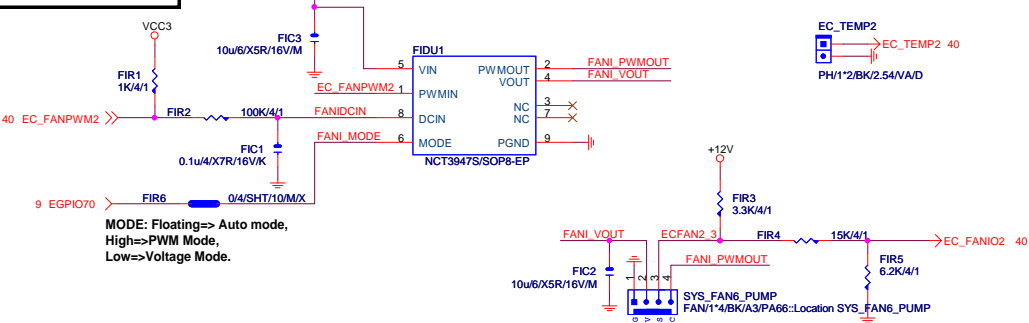
CPU_OPT



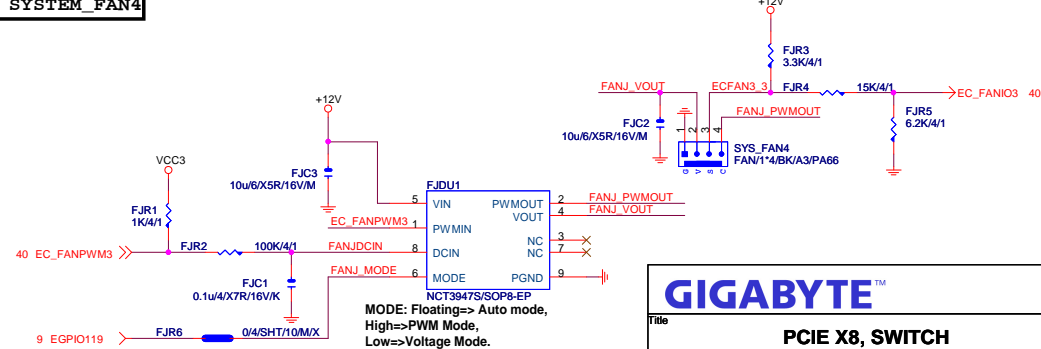
SYSTEM FAN5 PUMP



SYSTEM FAN6 PUMP



SYSTEM_FAN4

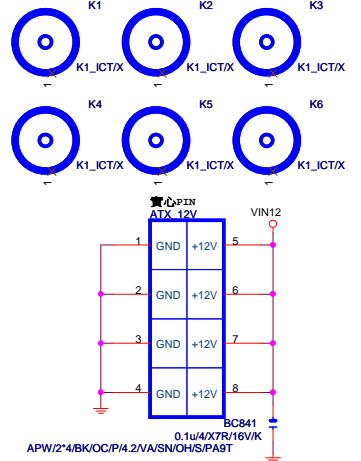
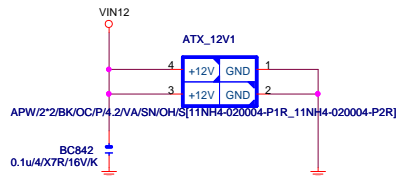
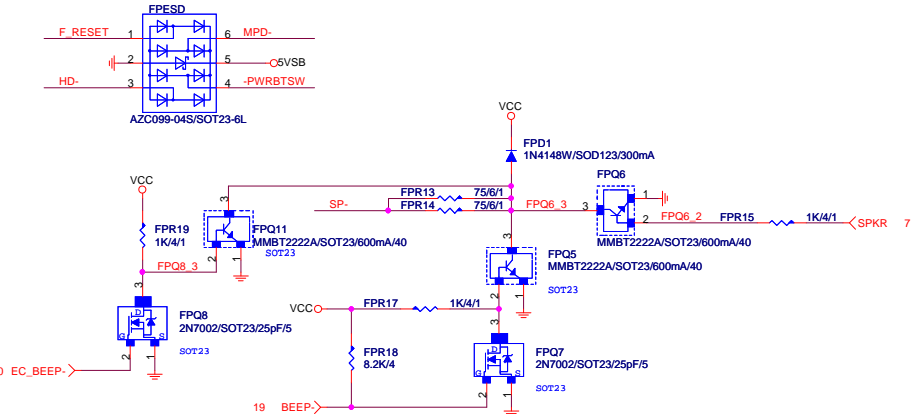
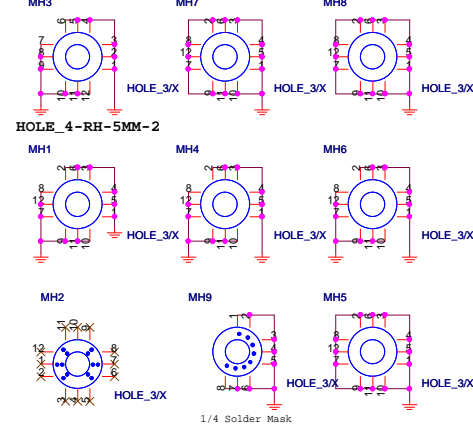
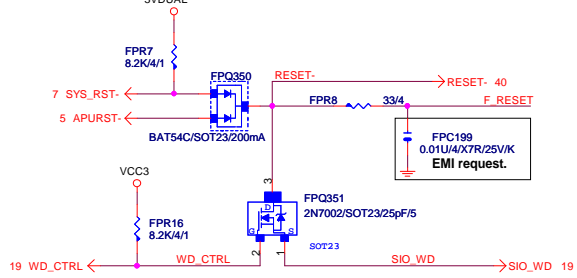
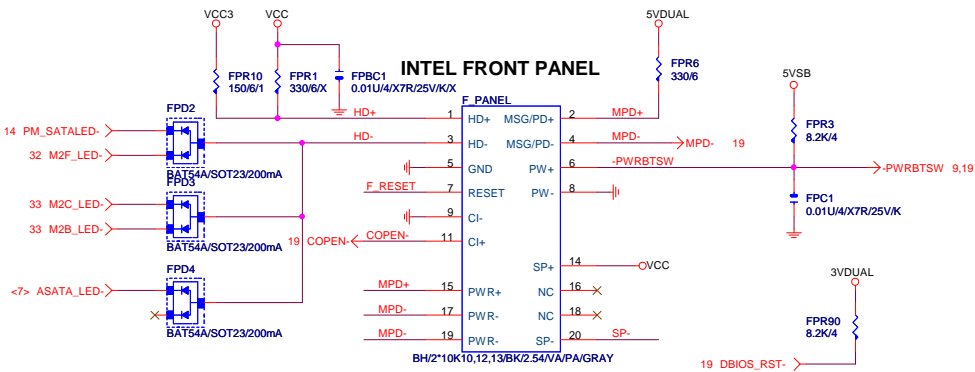


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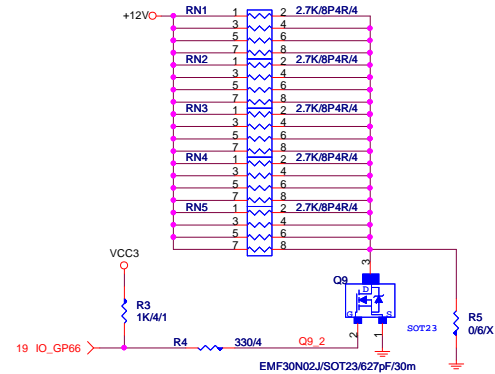
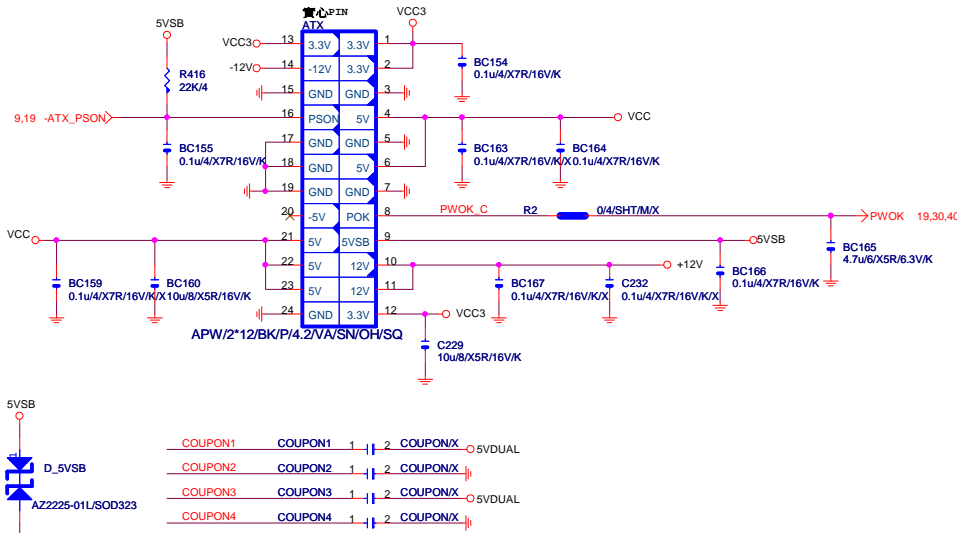
Title	PCIE X8, SWITCH
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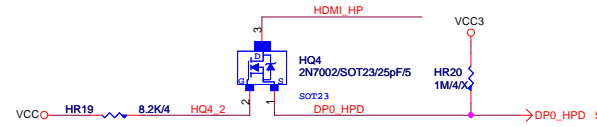
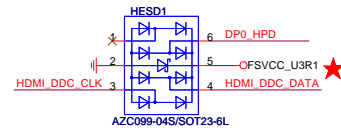
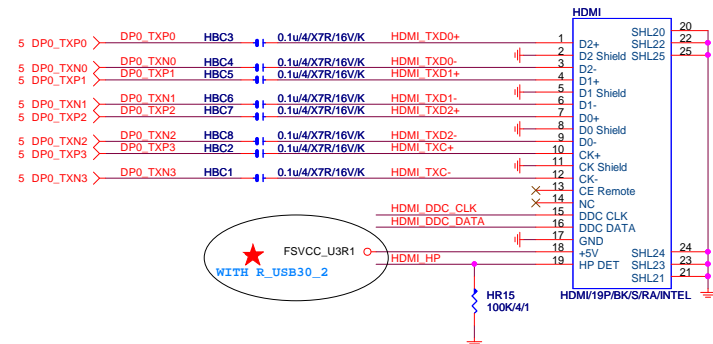
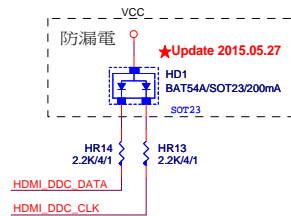
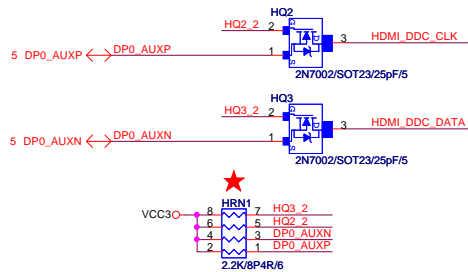
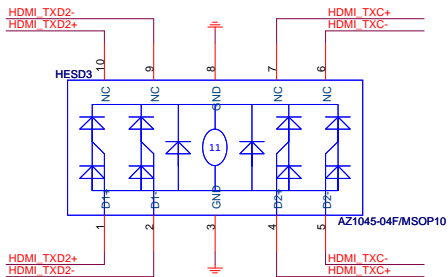
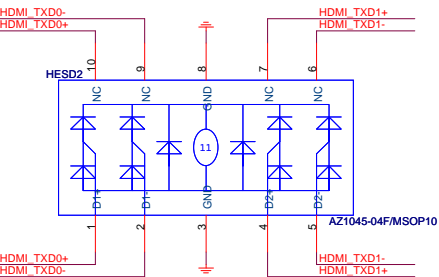
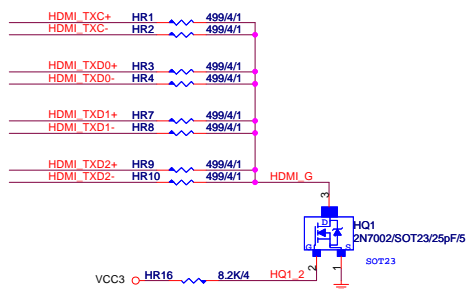
Size Custom	Document Number X570 AORUS PRO WIFI	Rev 1.0
Date: Tuesday, May 14, 2019	Sheet 49 of 55	

KBL FAN LOCATION MAP REFER TO PAGE.27



ATX POWER CONNECTOR

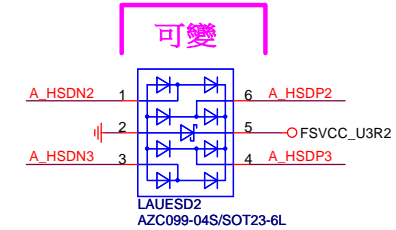




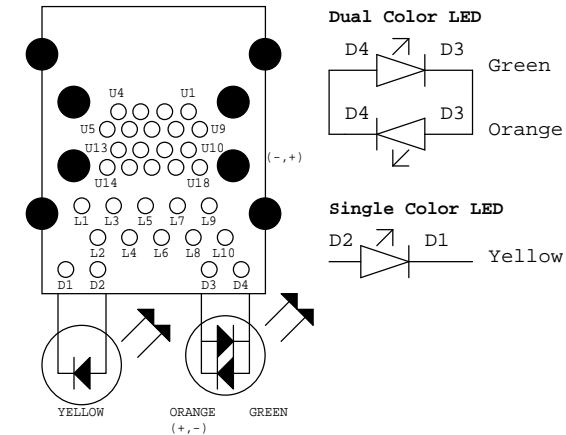
GIGABYTE™			
Title			
Realtek RTL8125			
Size	Document Number	Rev	
Custom	X570 AORUS PRO WIFI	1.0	
Date:	Tuesday, May 14, 2019	Sheet	51 of 55

USB_LAN CONNECTOR R1.0

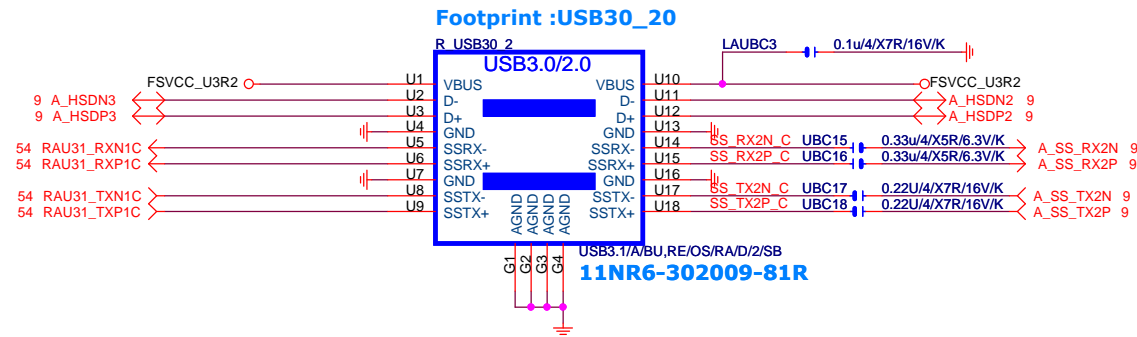
RMA ESD PROTECT note:可變更USB_NAME



USB30_LAN LAYOUT示意圖

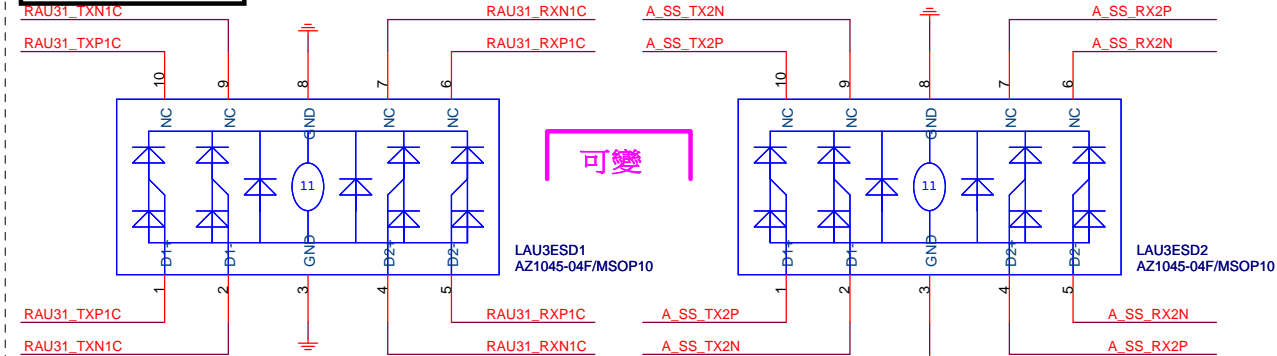


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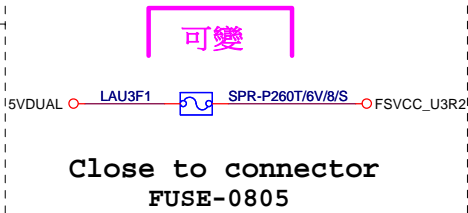


LA_MDI-->100歐姆:[20/4/8/4/20]

RMA ESD PROTECT note:可變更USB_NAME



USB POWER note:可變更FUSE



EMI SHORT PAD PS:視EMI需求

LAN POWER note: lan power連接及電流

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Title: Realtek RTL8125

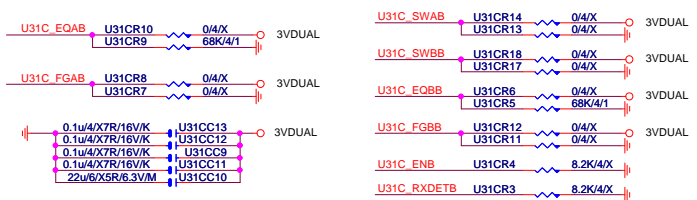
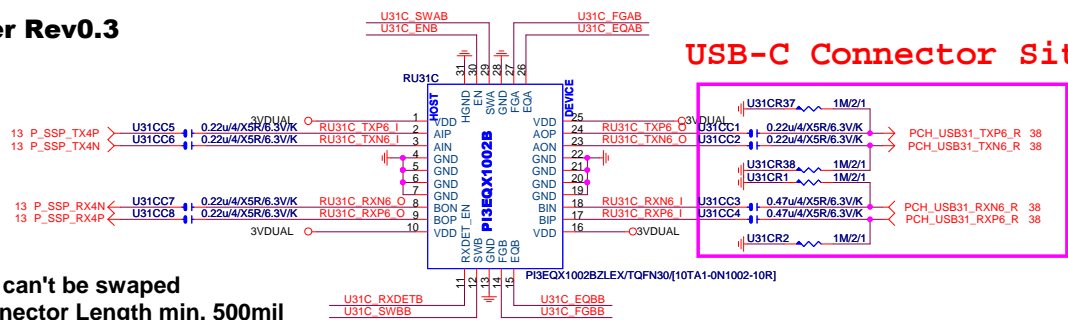
Size: Custom Document Number: X570 AORUS PRO WIFI Rev: 1.0

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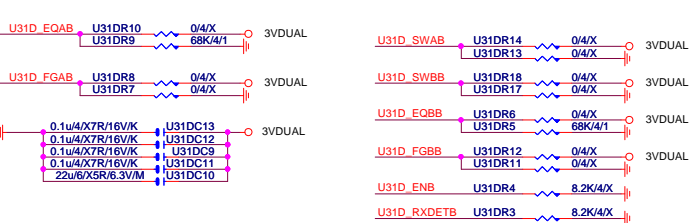
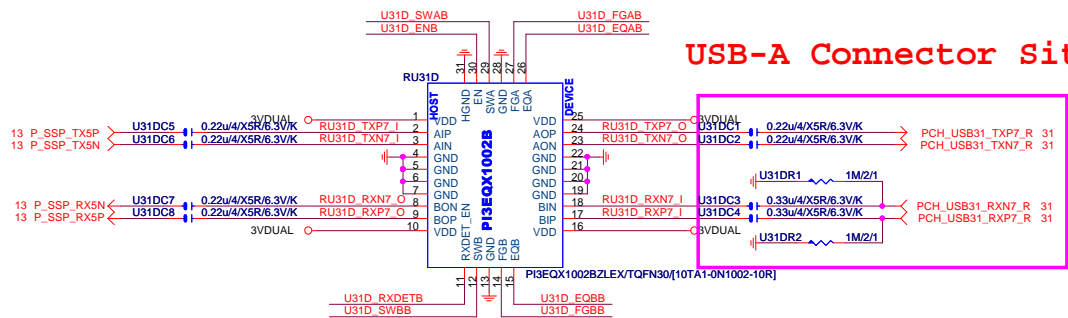
TypeA Redriver Rev0.3

- Layout Rule:
- 1. Differential Pair can't be swapped
 - 2. Redriver to Connector Length min. 500mil

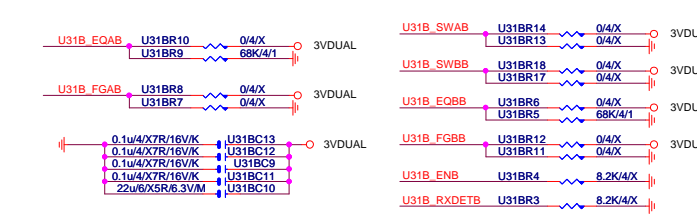
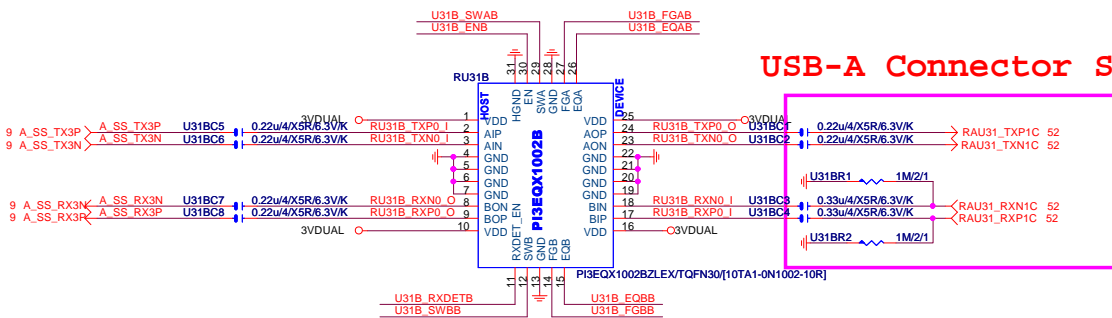
USB-C Connector Site



USB-A Connector Site



USB-A Connector Site



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Title

PI3EQX1002B Redriver/DC

Size

Custom

Date

Tuesday, May 14, 2019

Document Number

X570 AORUS PRO WIFI

Rev

1.0

Sheet

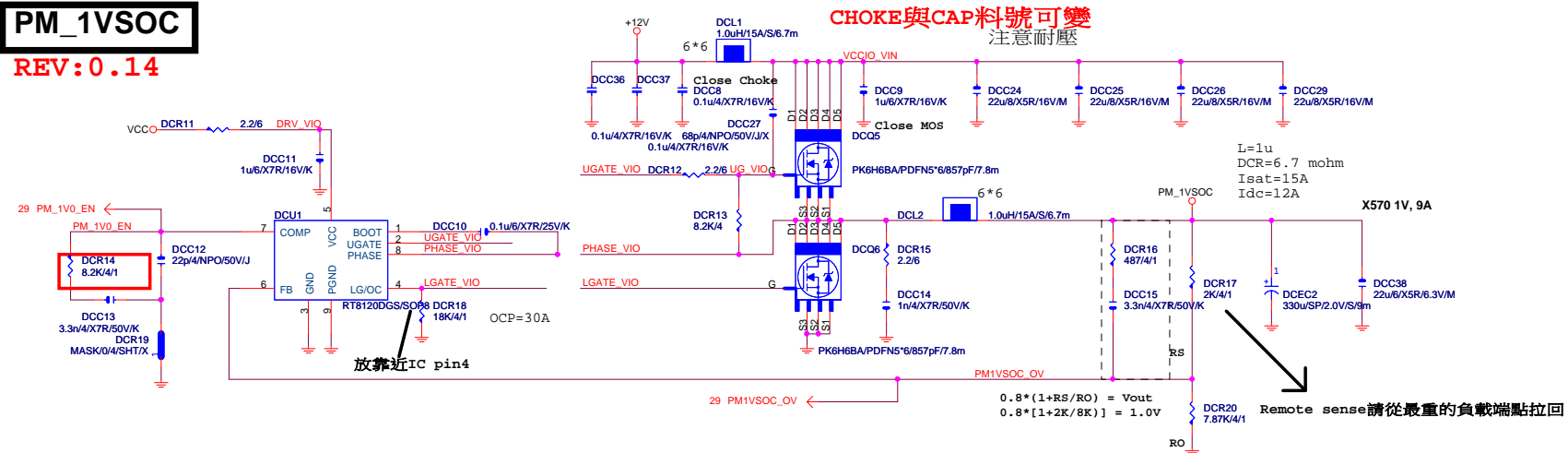
54

of

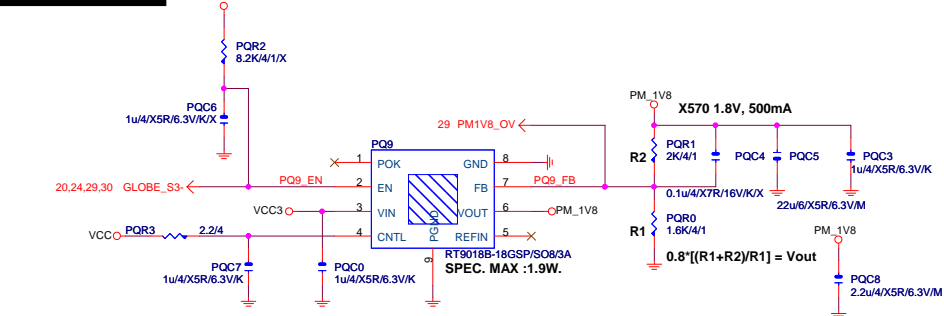
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PM_1VSOC

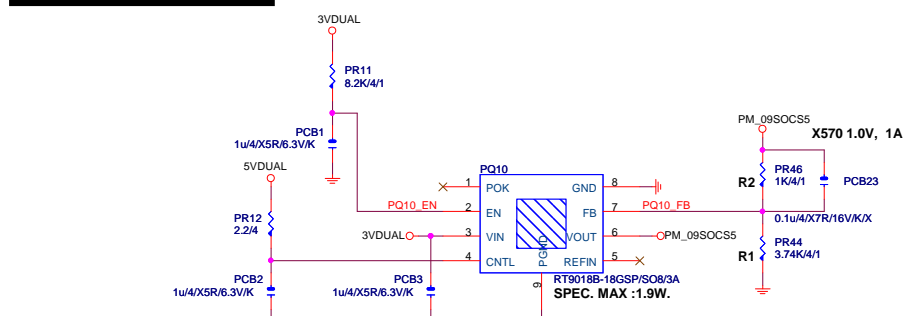
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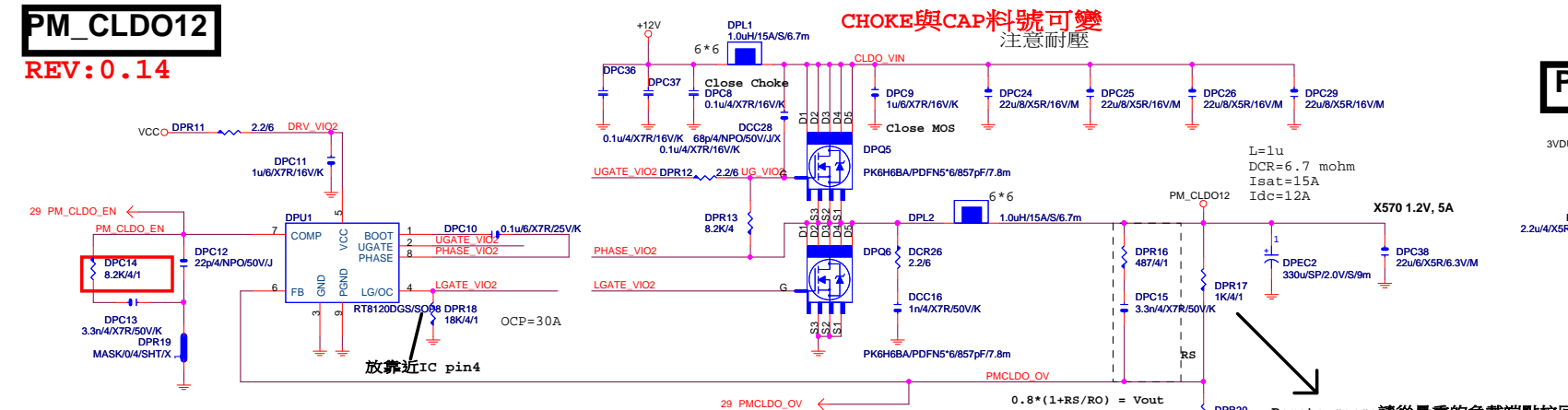


PM_1VSOC_S5



PM_CLDO12

REV:0.14



PM_1V8_SUS

